

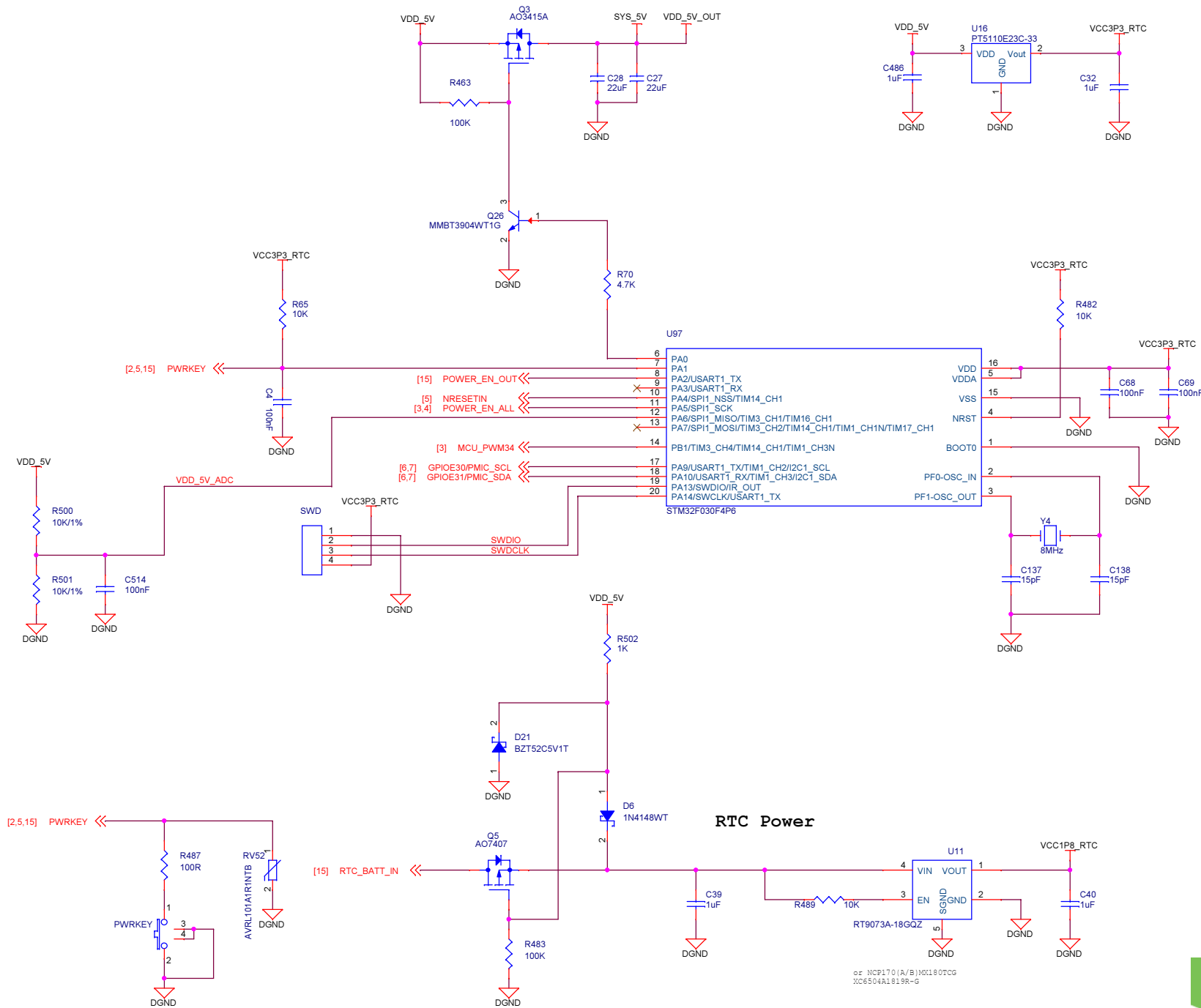
Core4418

Revision

1711 , First Release

FRIENDLYARM

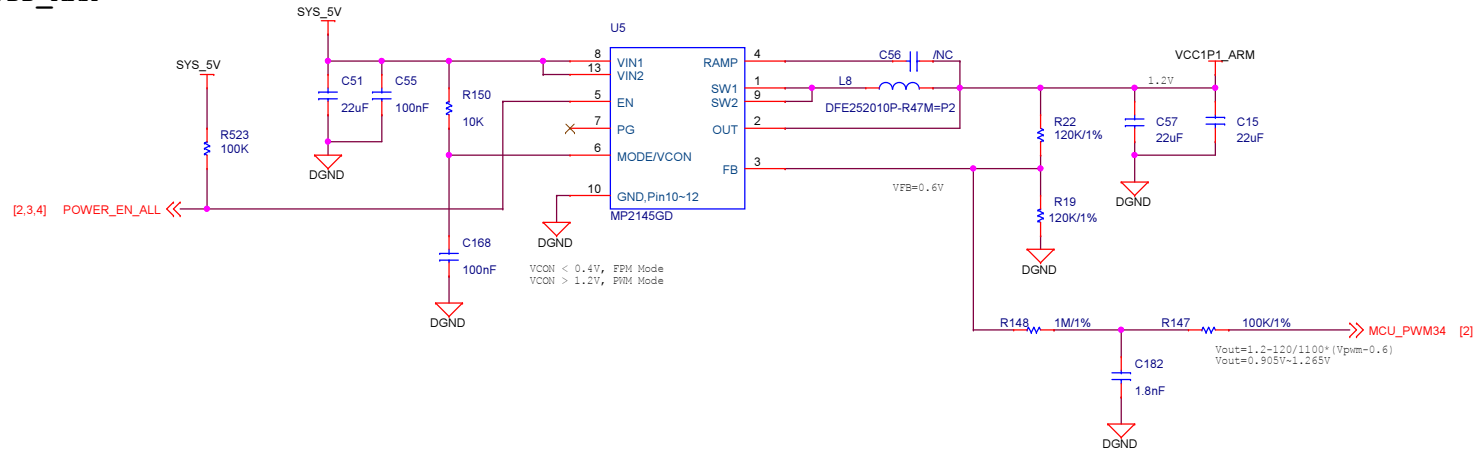
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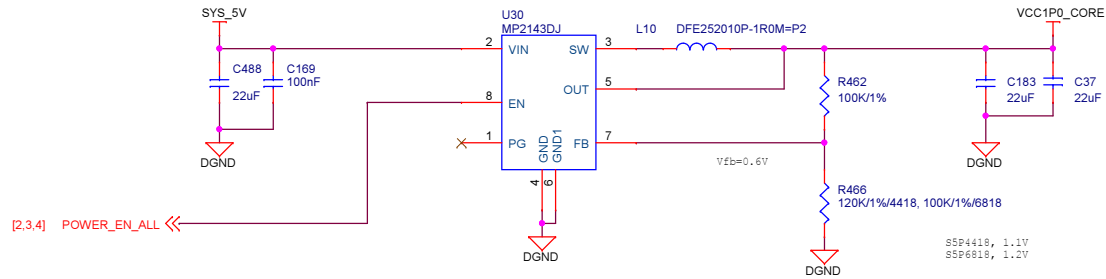
RTC Power

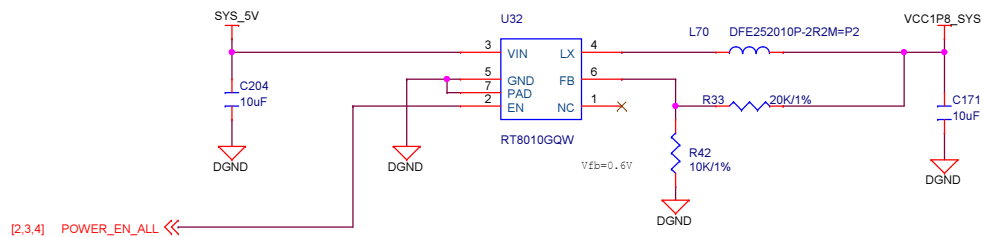
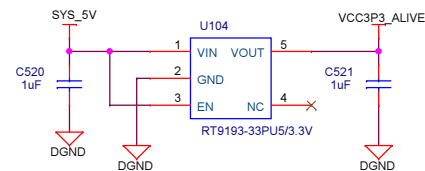
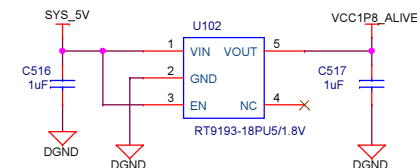
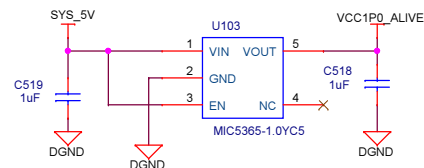
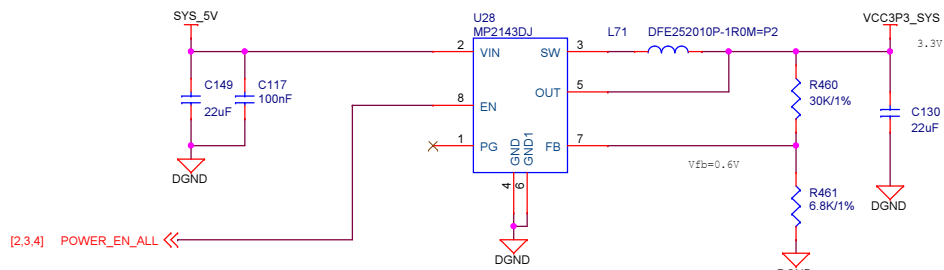
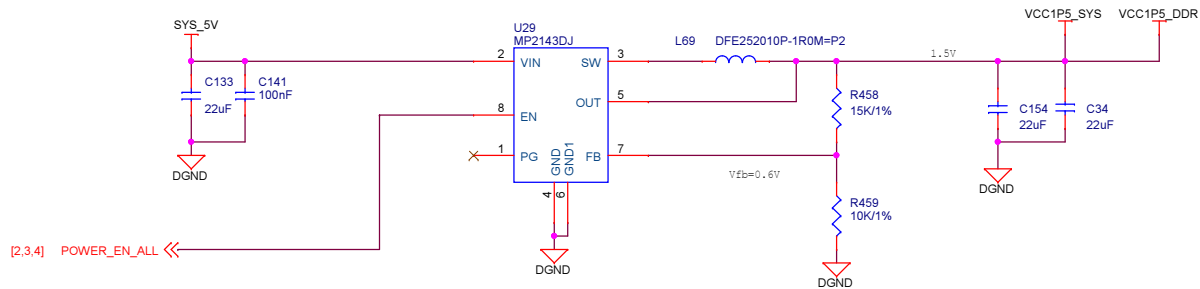


VDD_ARM



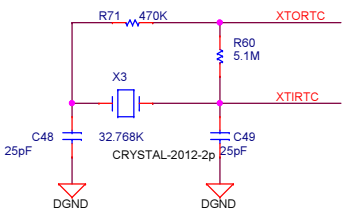
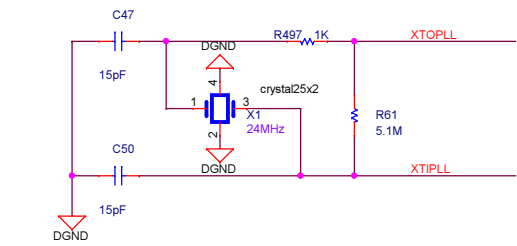
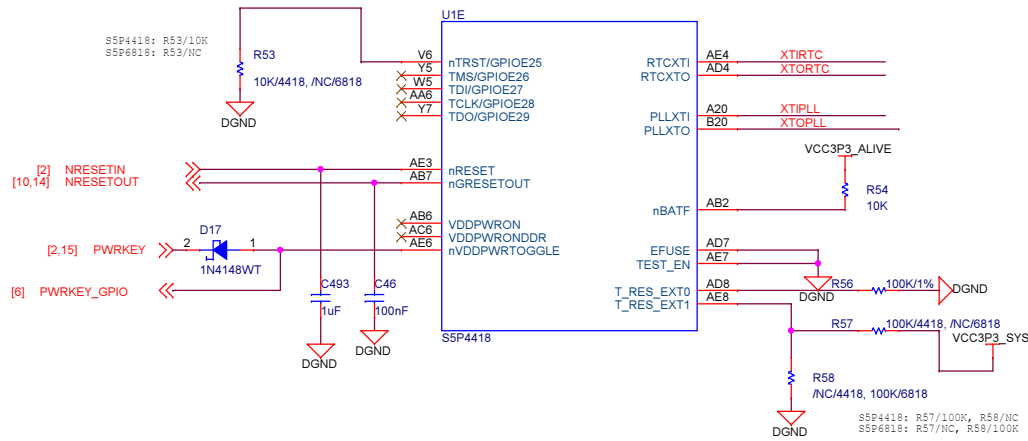
VDD_CORE



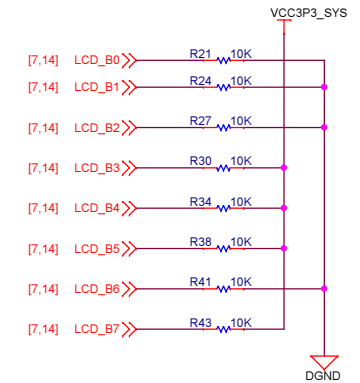
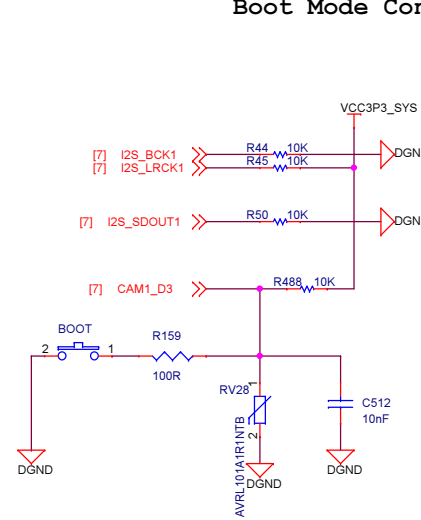


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System Reset, Clocks



Boot Mode Config



BOOT MODE OPTION

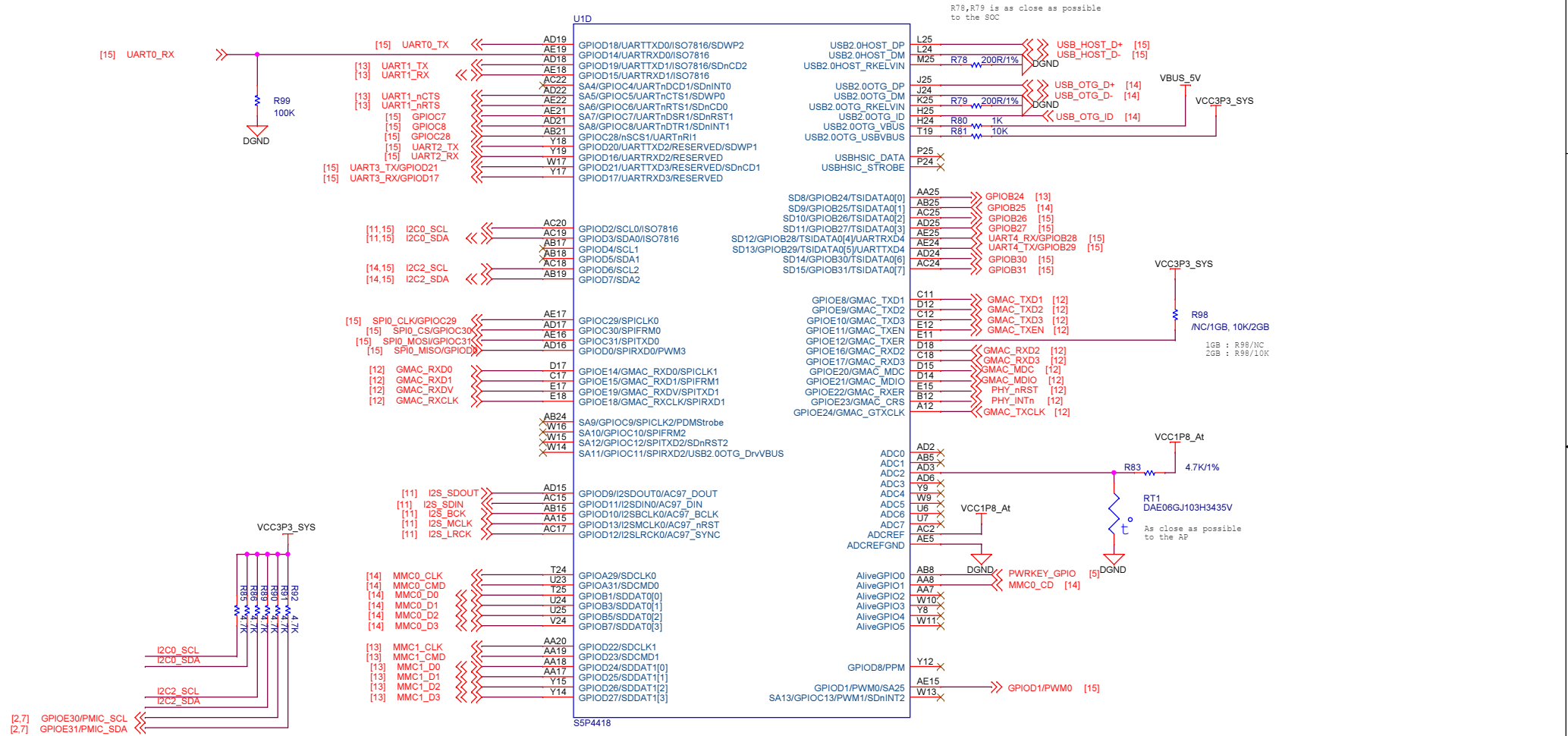
	eMMC	SPI	USB	NAND
SD0	HIGH	LOW	LOW	HIGH
SD1	LOW	LOW	HIGH	HIGH
SD2	HIGH	HIGH	HIGH	HIGH
SD4	LOW	HIGH		
SD5	LOW	LOW		

Boot media port select (SPI, eMMC)

	CH0	CH1	CH2
SD3	LOW	HIGH	LOW
CAM1_D3	LOW	LOW	HIGH



AP Peripherals

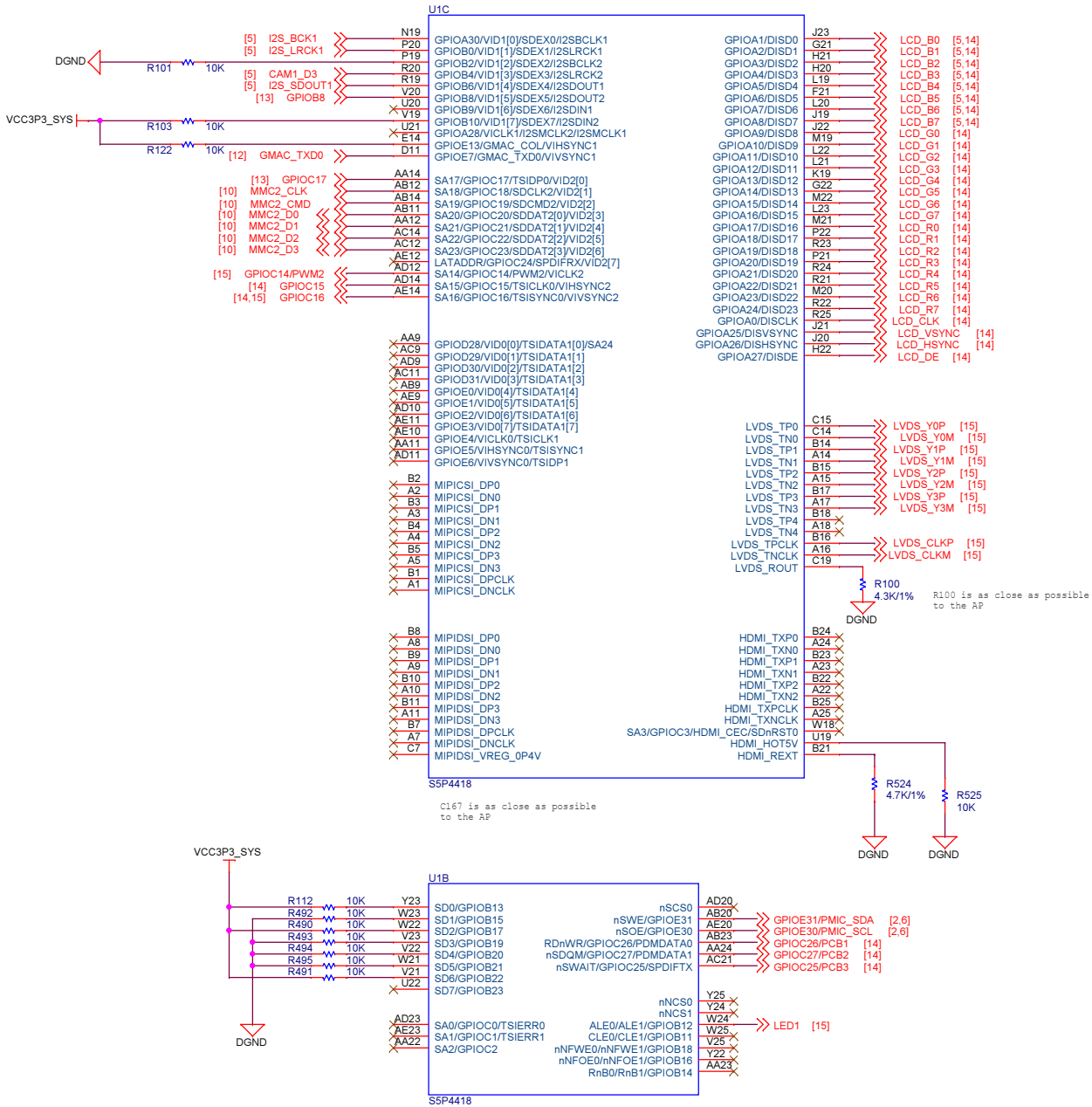


I2C CH0 : Camera
 I2C CH1 : HDMI EDID
 I2C CH2 : Touch
 PMIC_I2C : PMIC

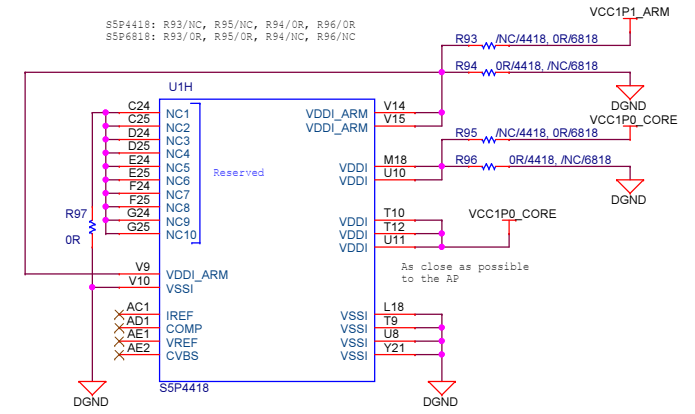


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AP VIP&Display

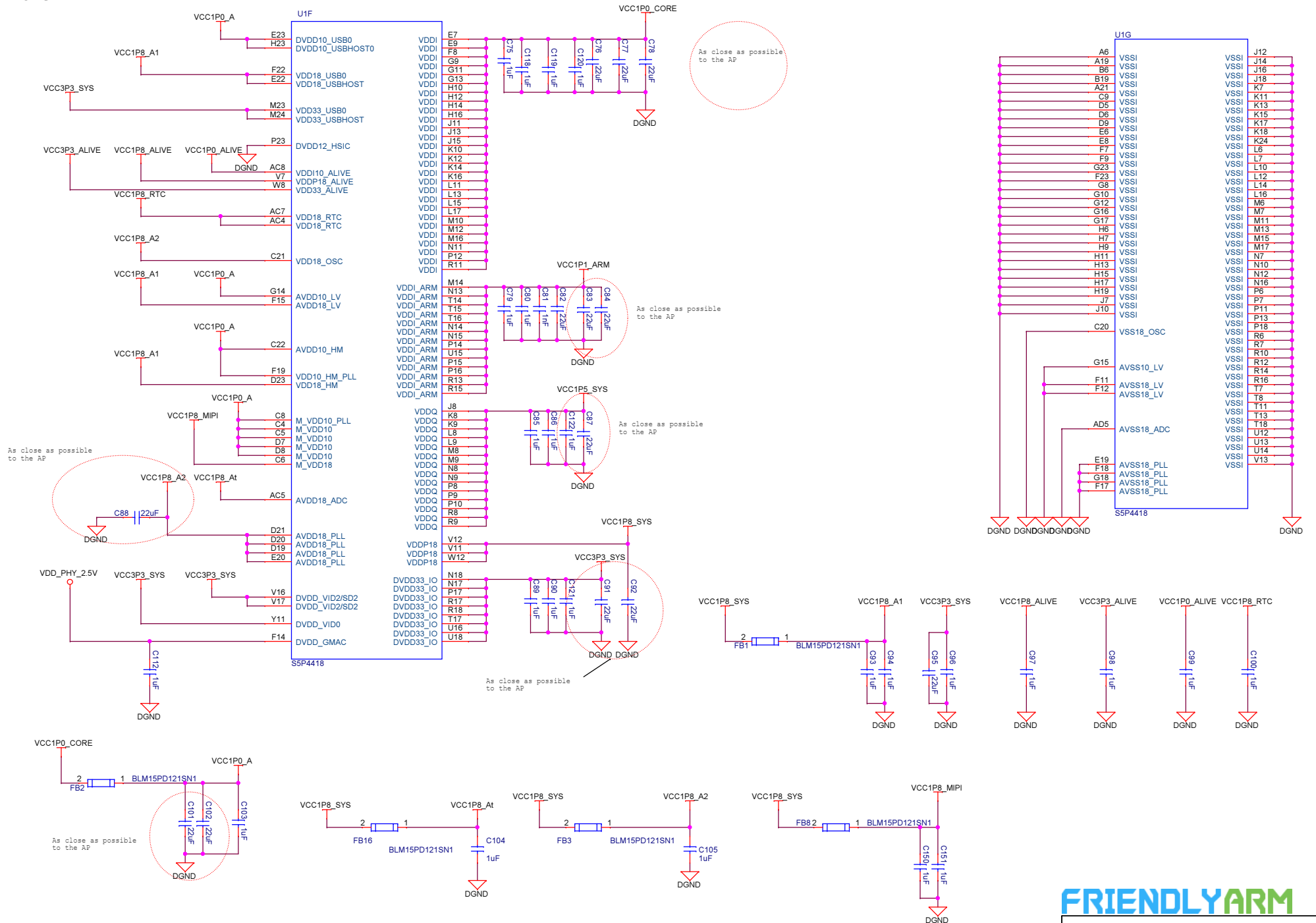


Reserved Function



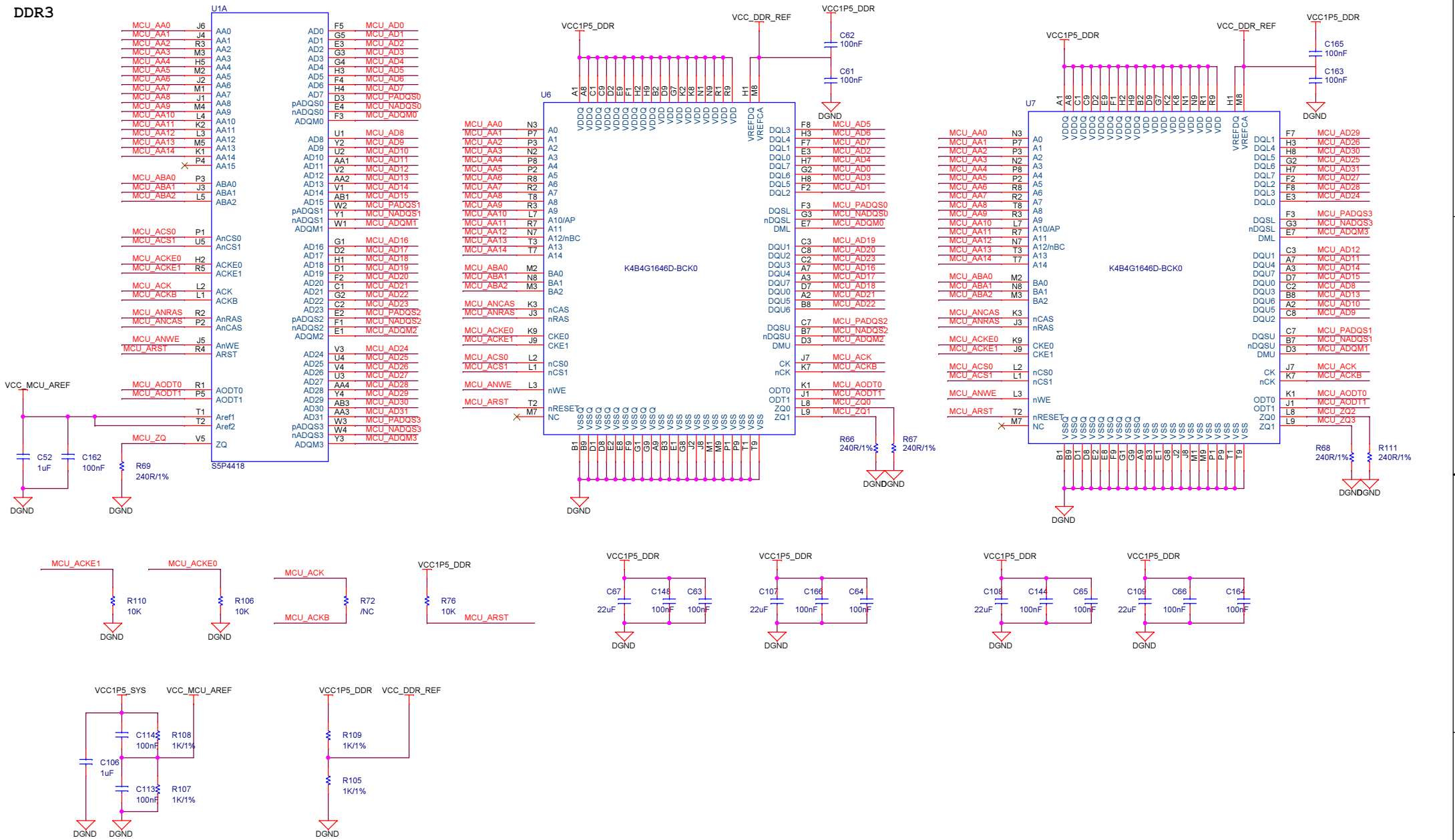
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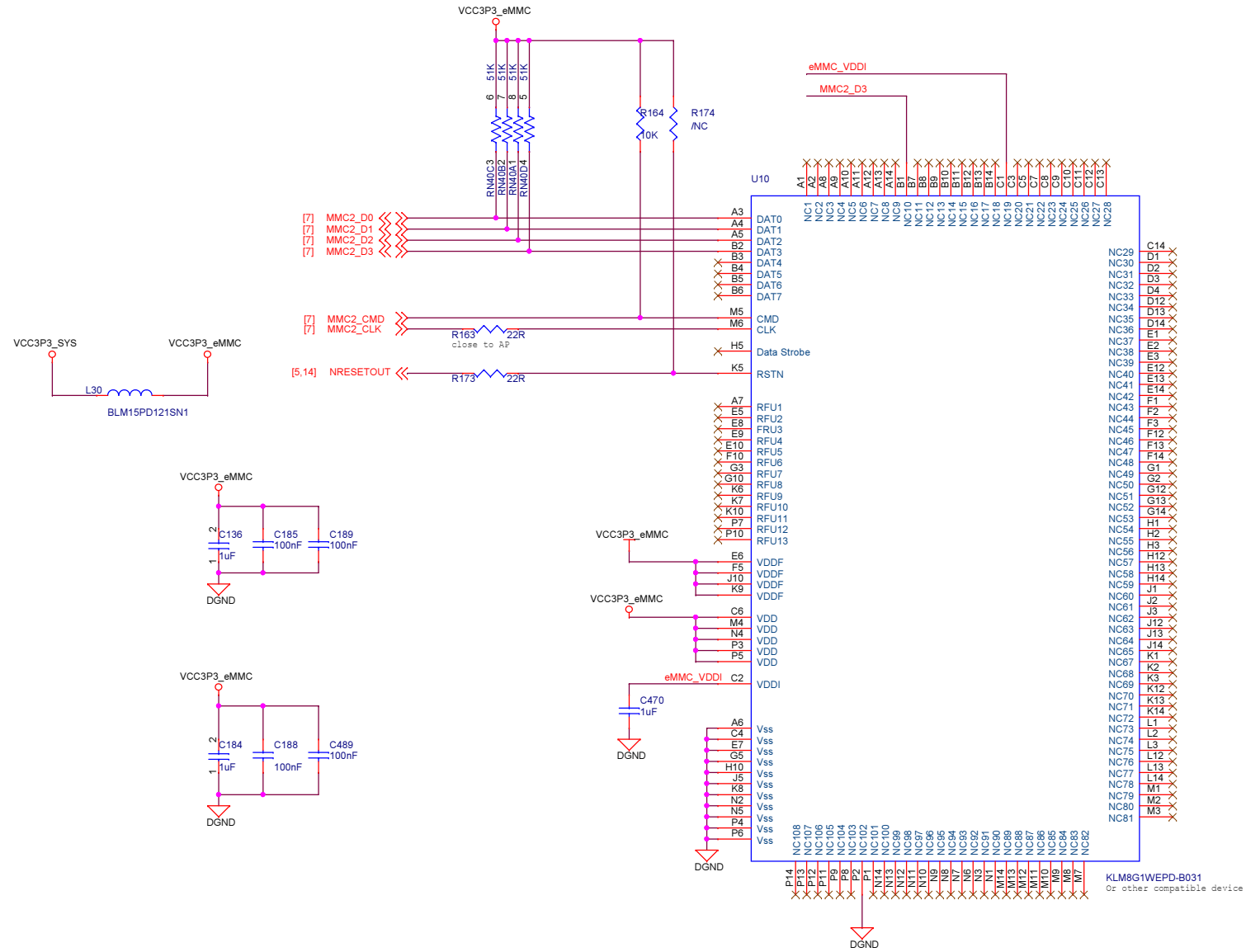
AP Power



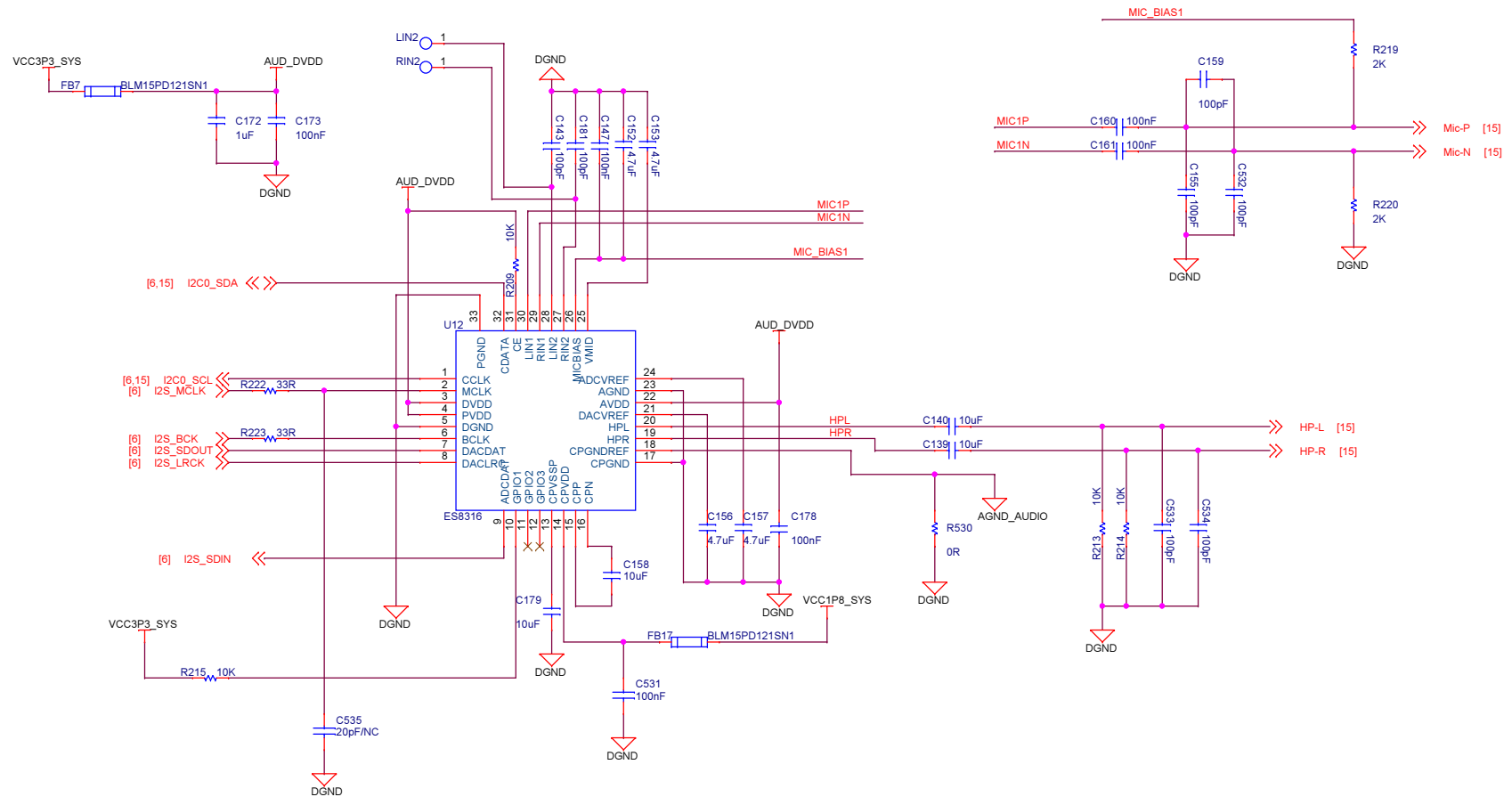
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DDR3

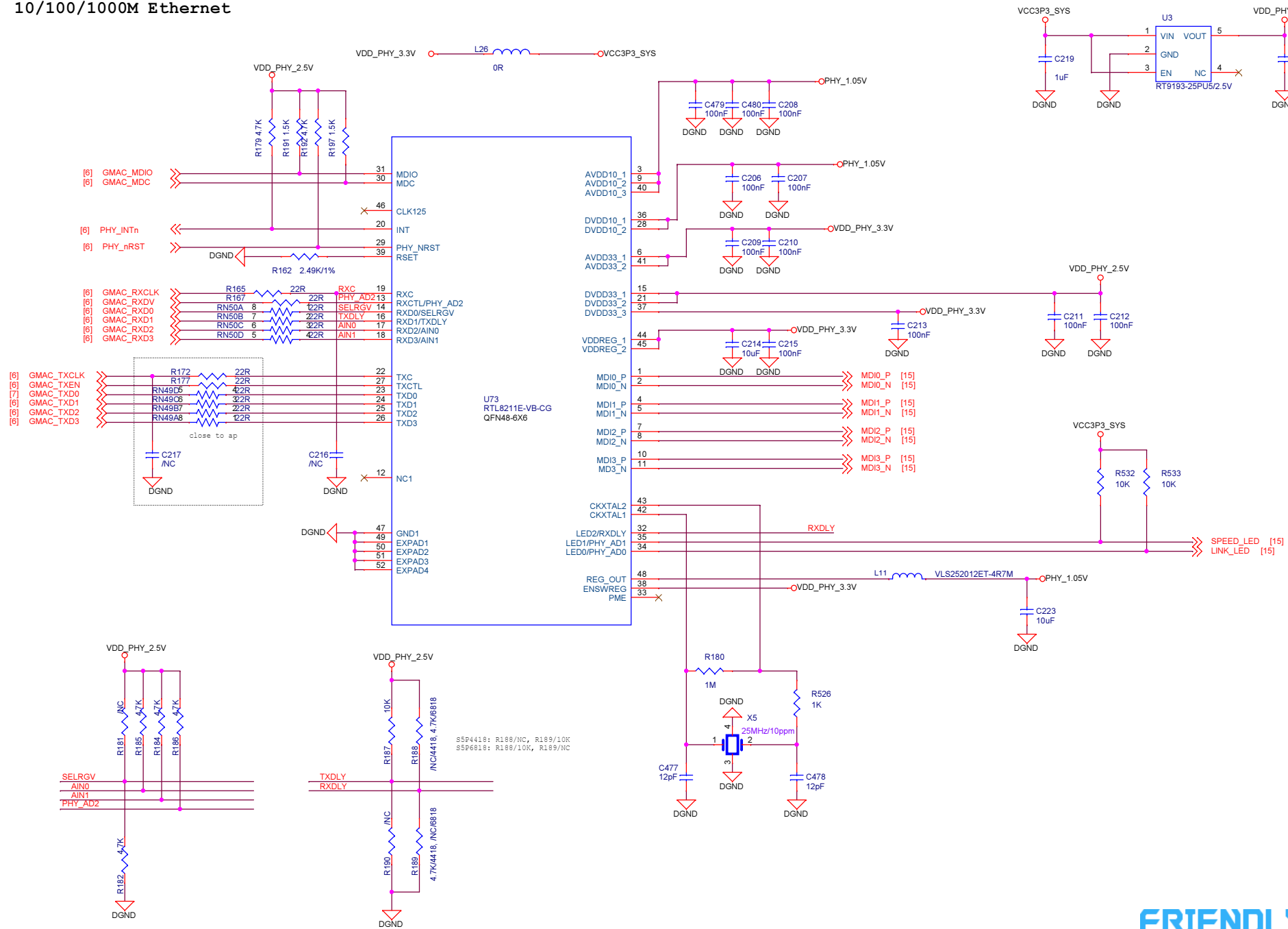




Audio

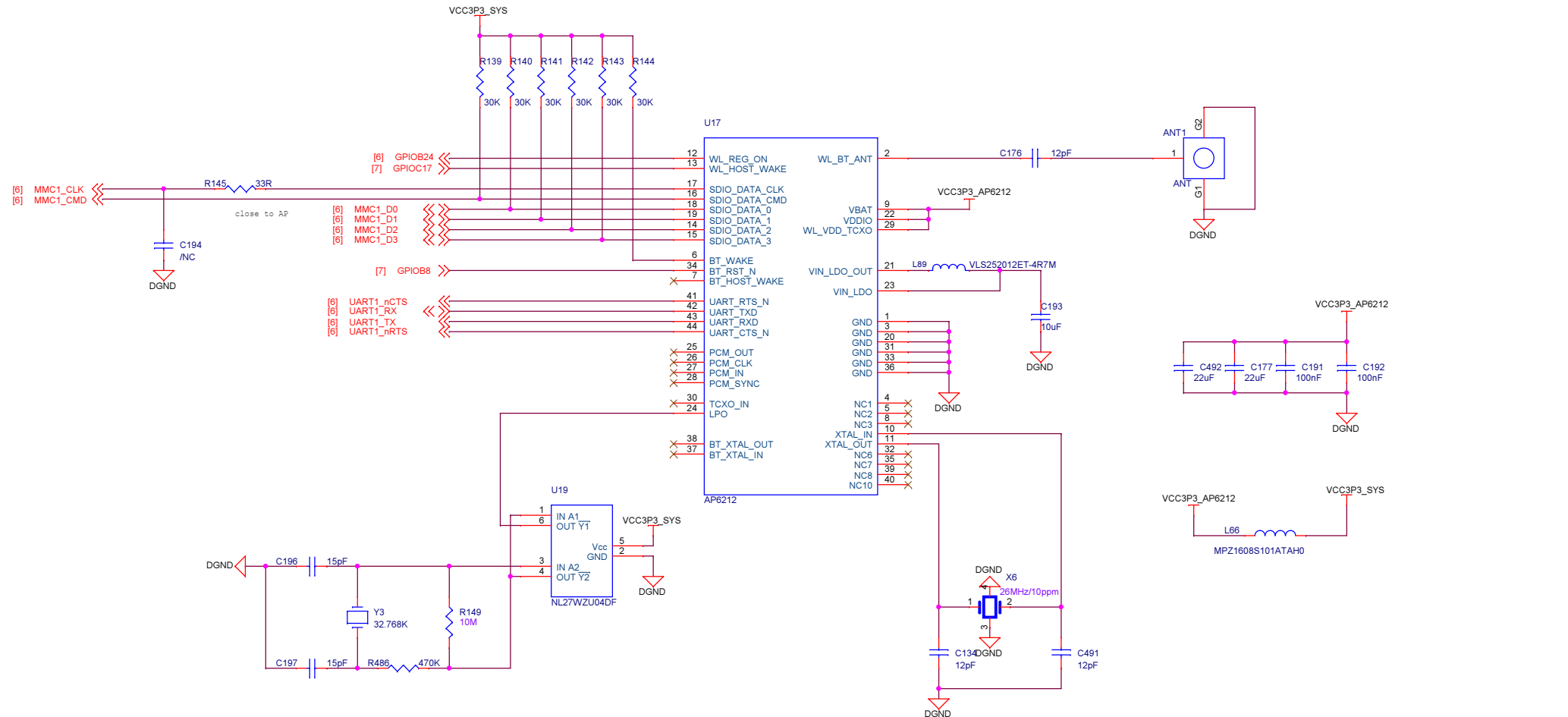


10/100/100M Ethernet



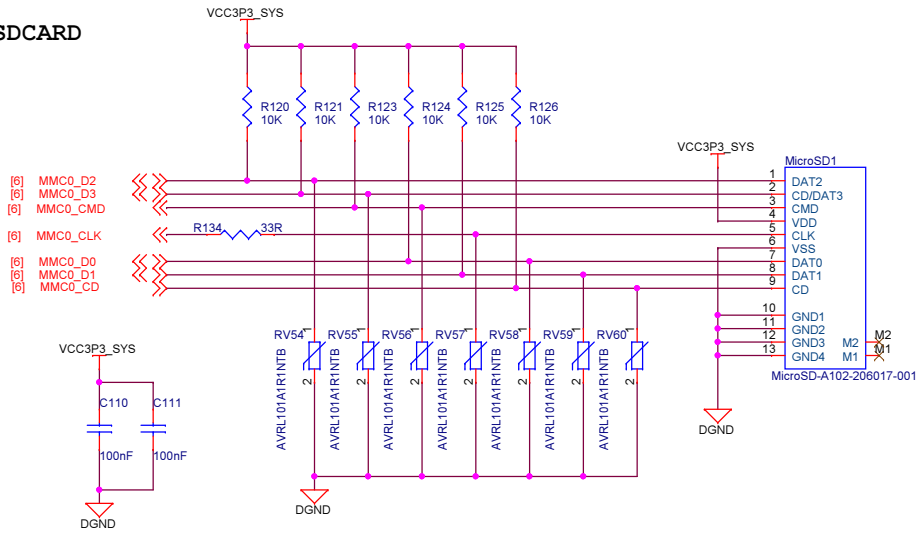
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Wi-Fi/Bluetooth

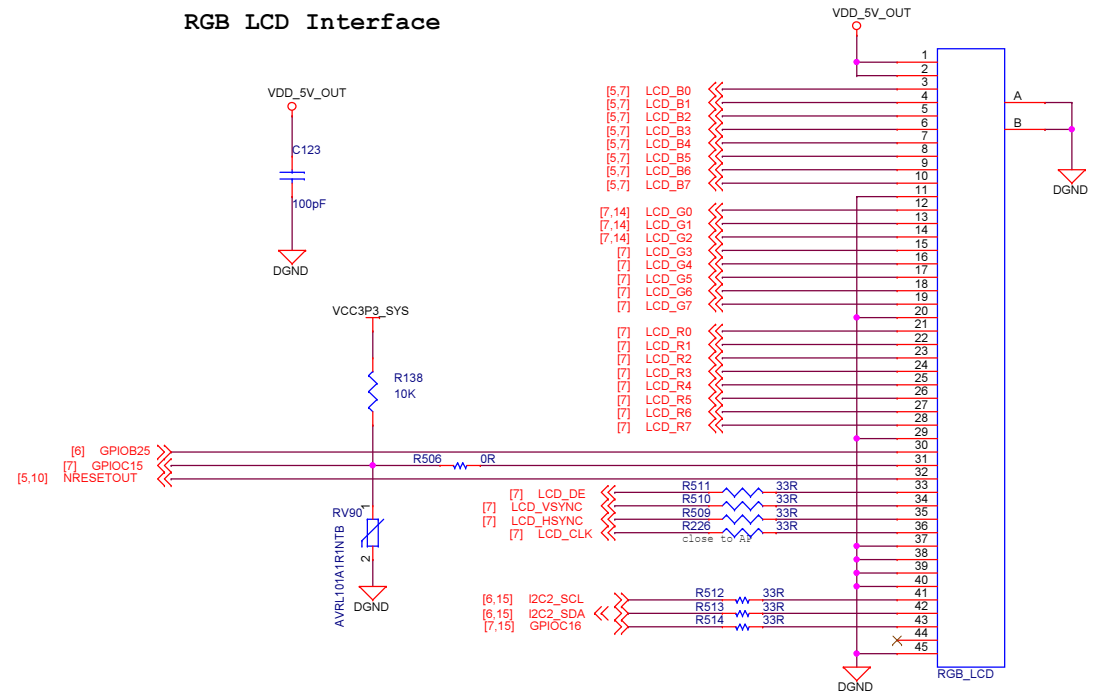


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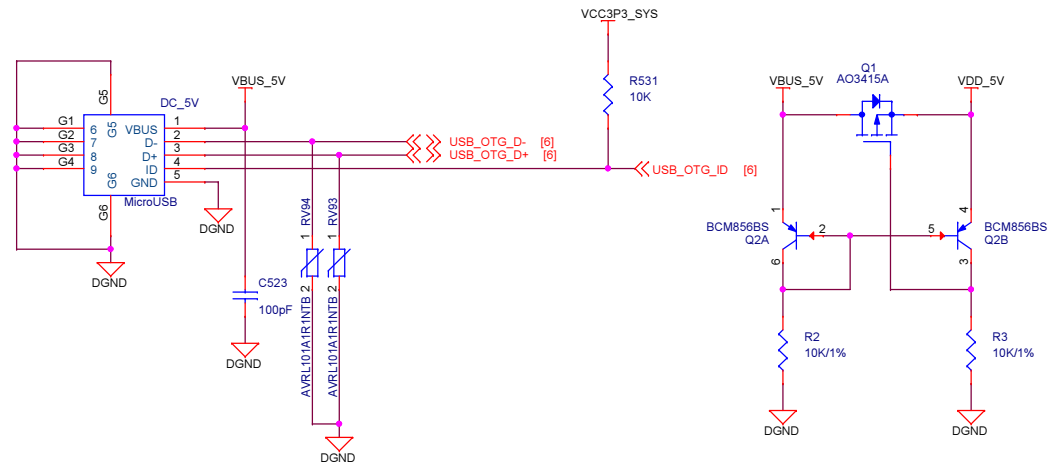
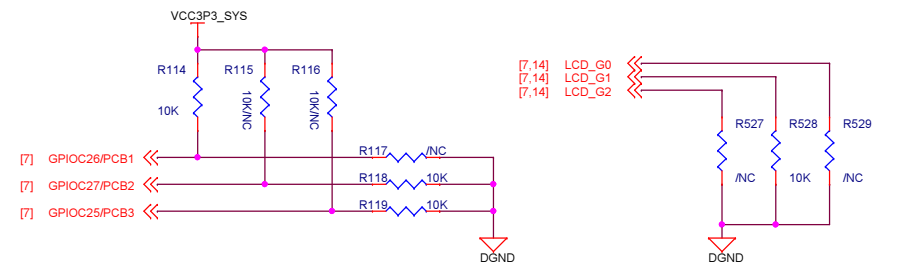
SDCARD



RGB LCD Interface

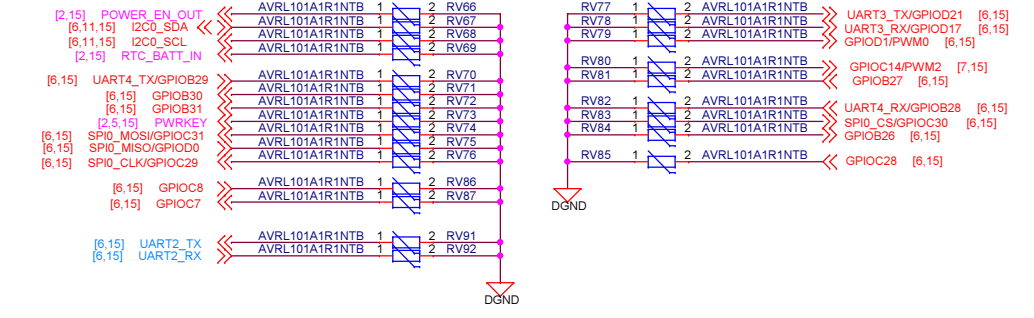
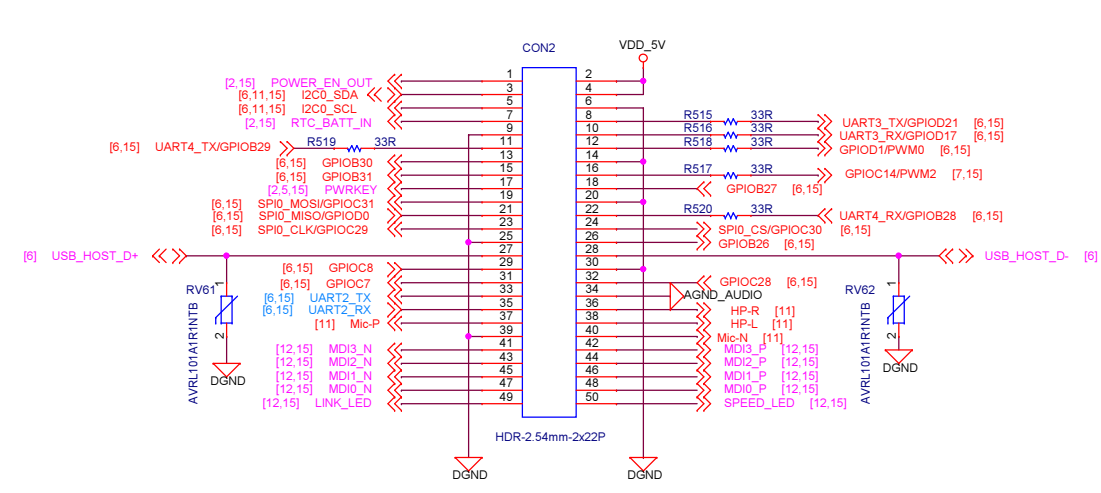


PCB Version

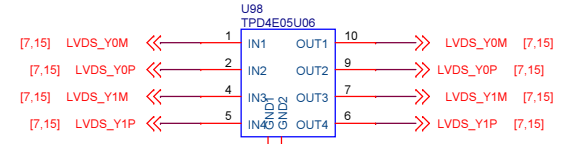
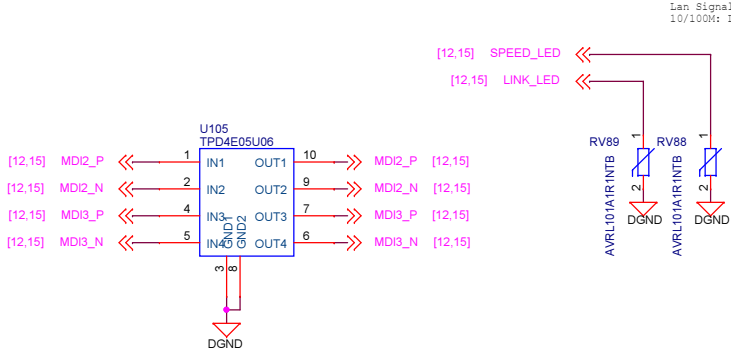
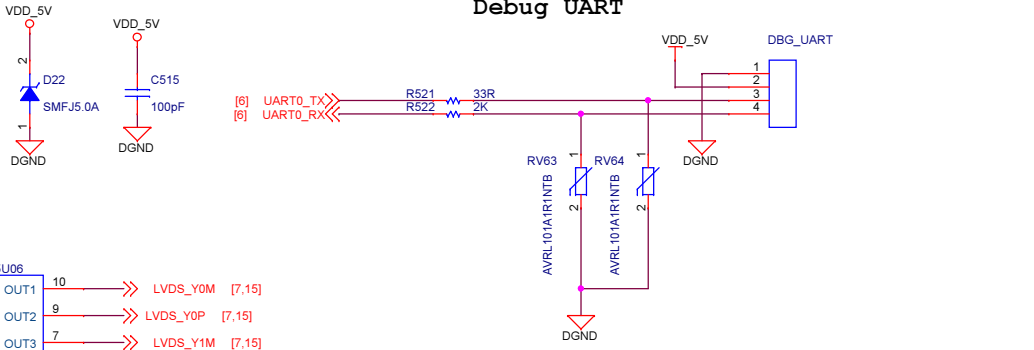


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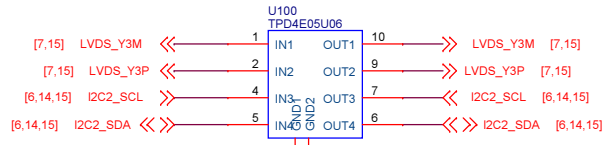
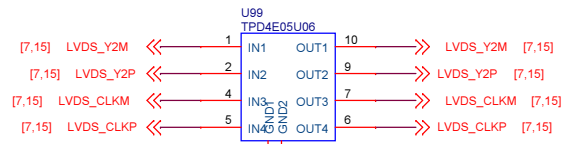
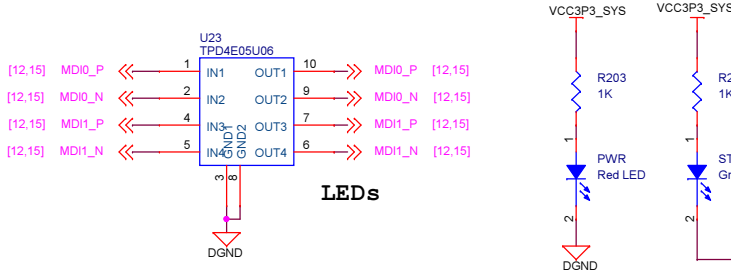
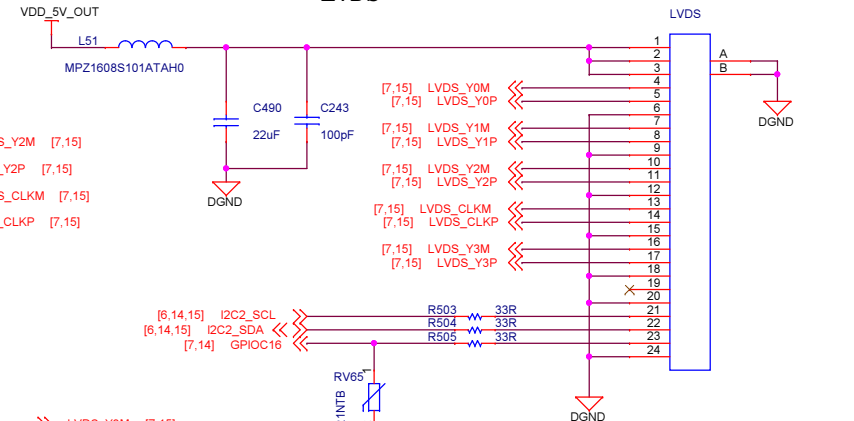
2.54mm Header



Debug UART



LVDS



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