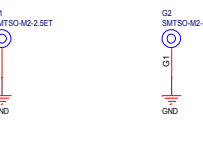
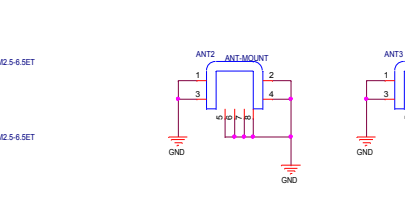
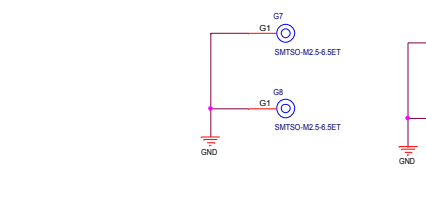
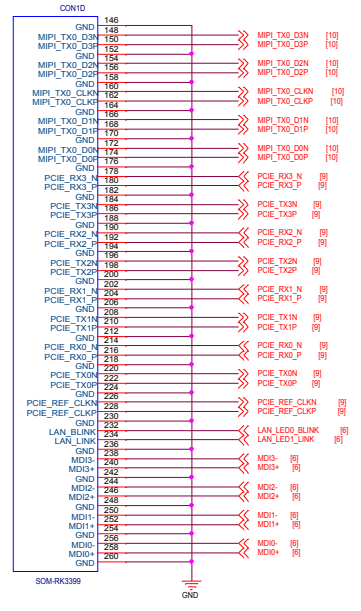
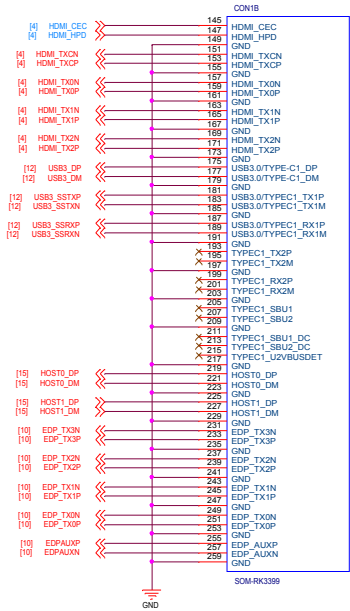
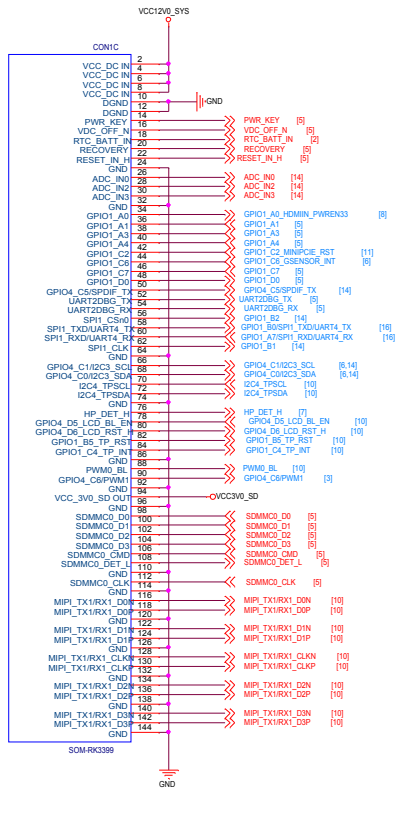
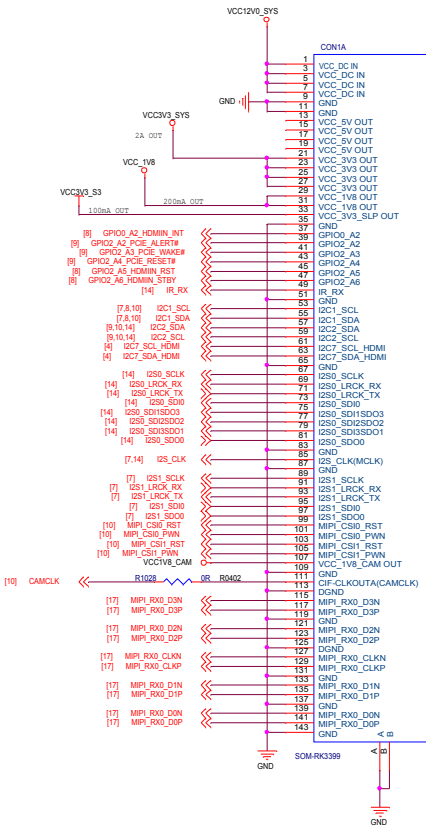
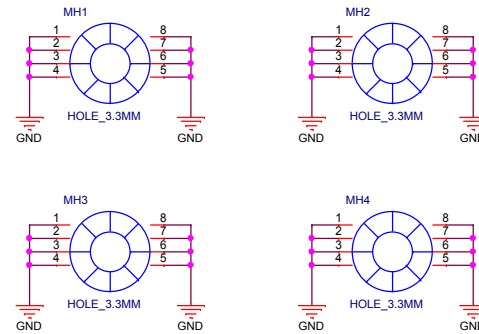
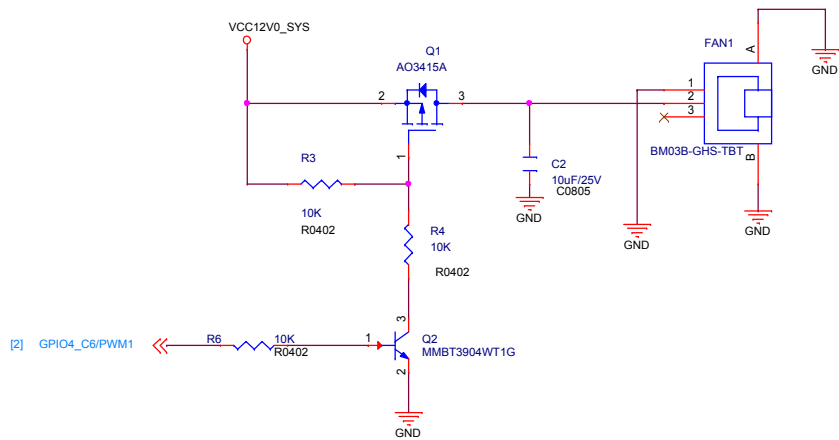
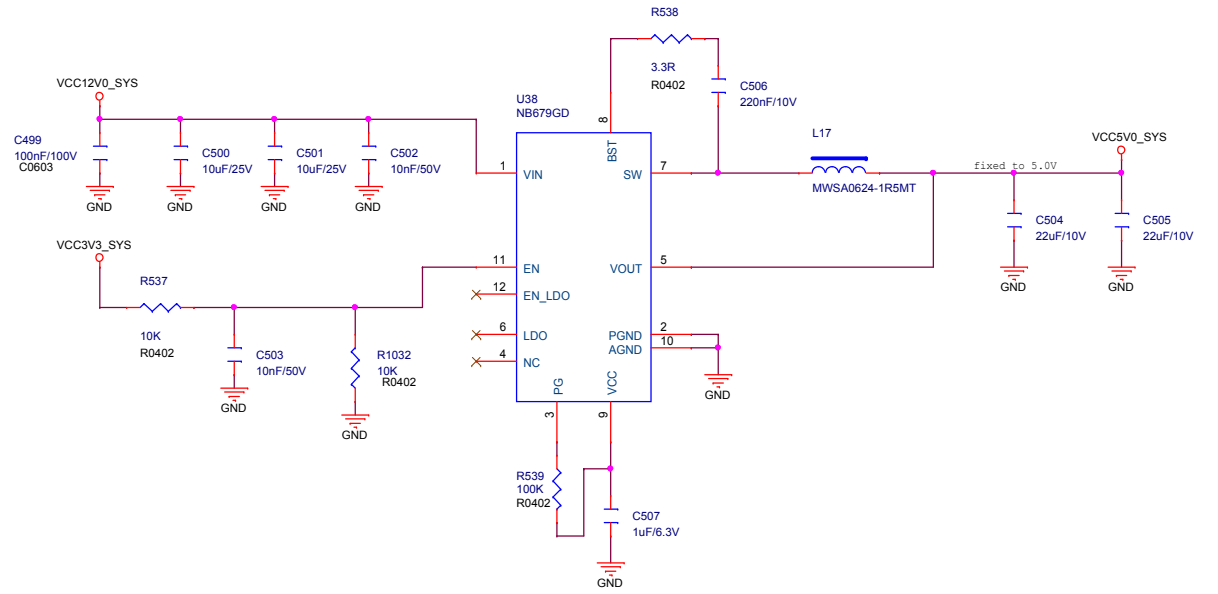
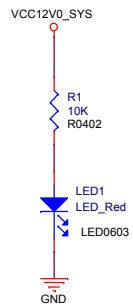
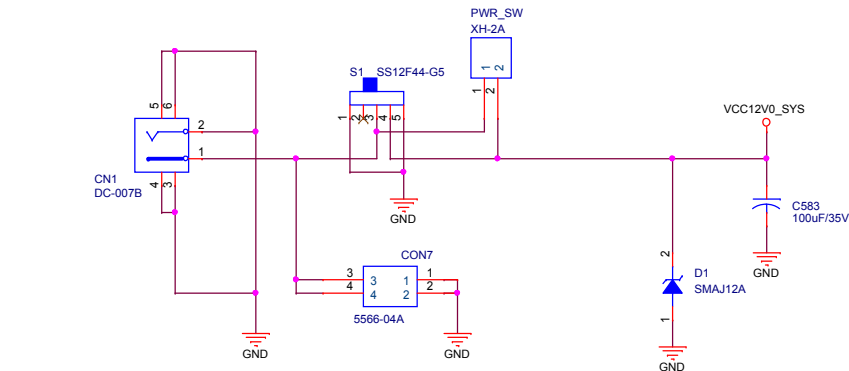


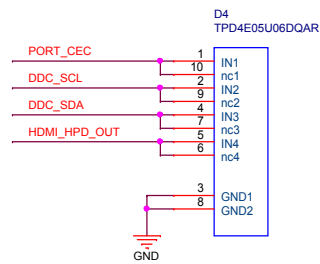
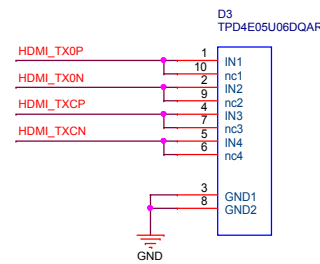
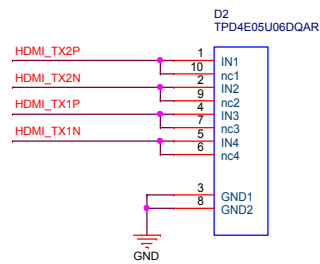
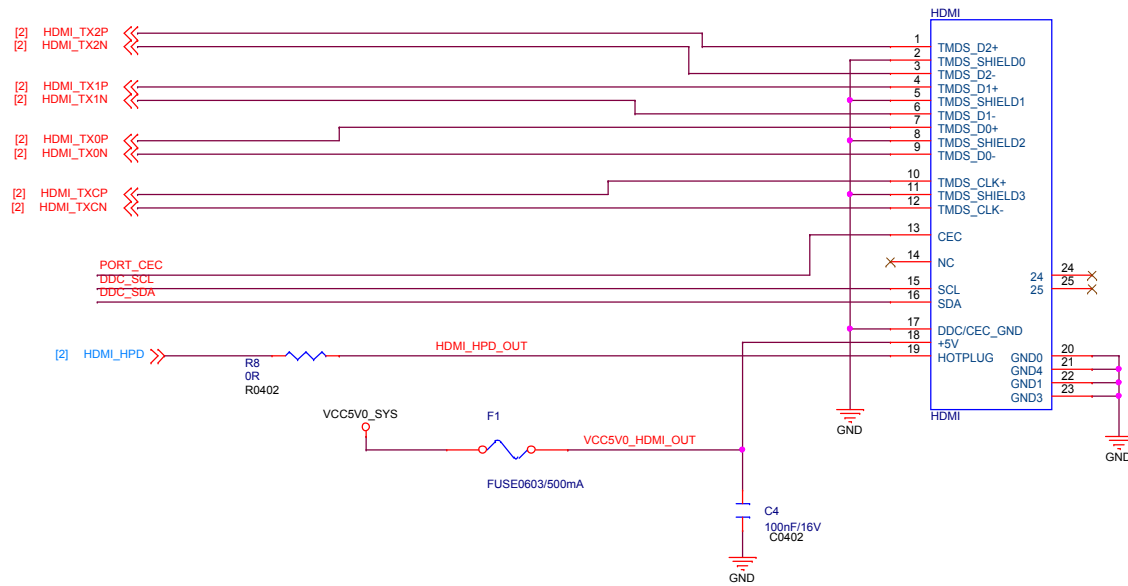
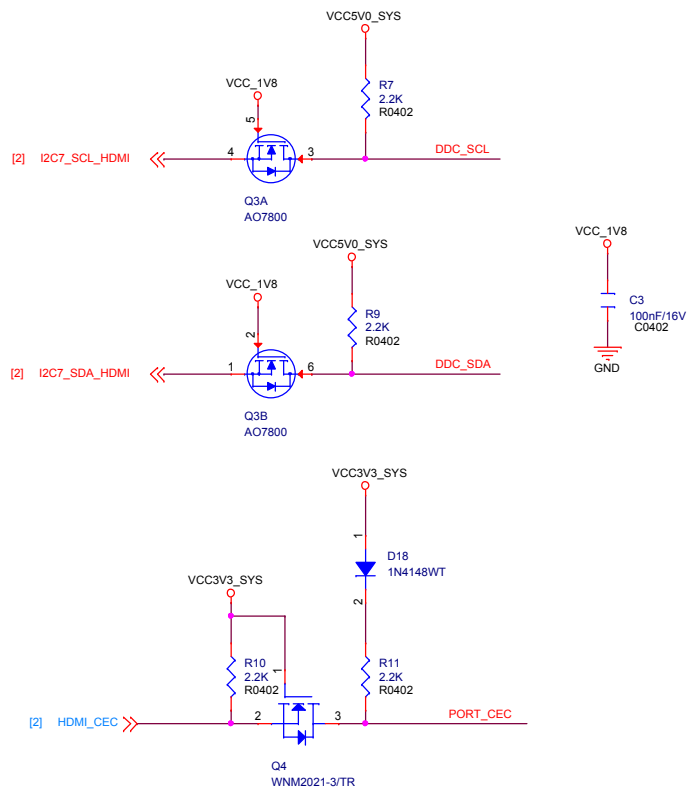
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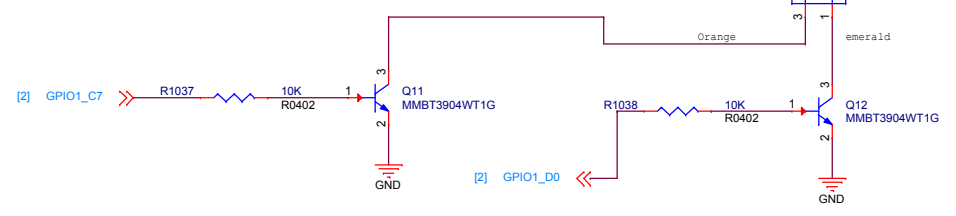
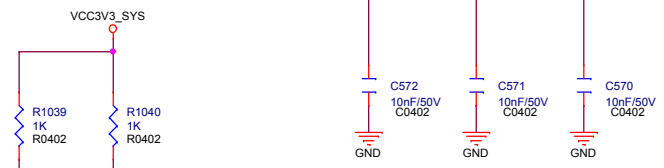
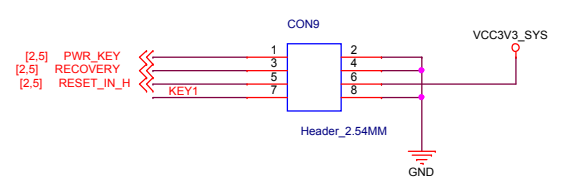
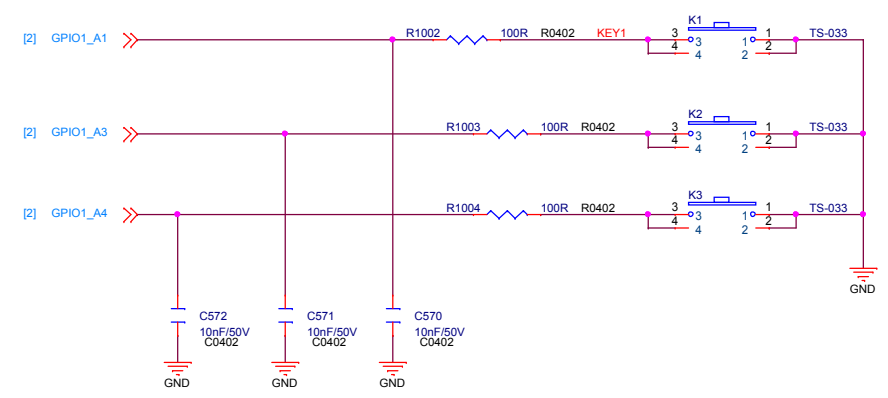
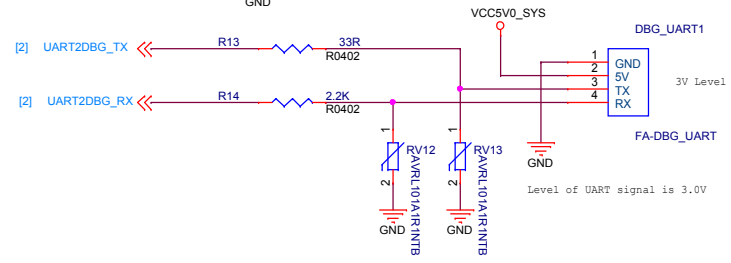
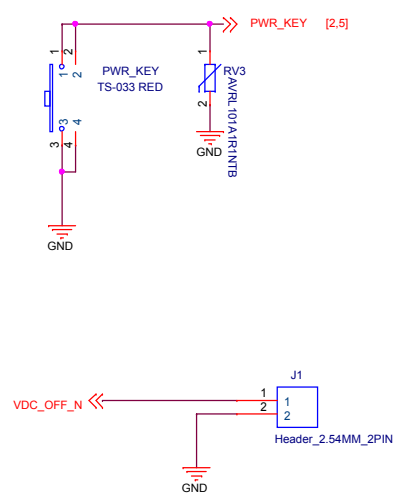
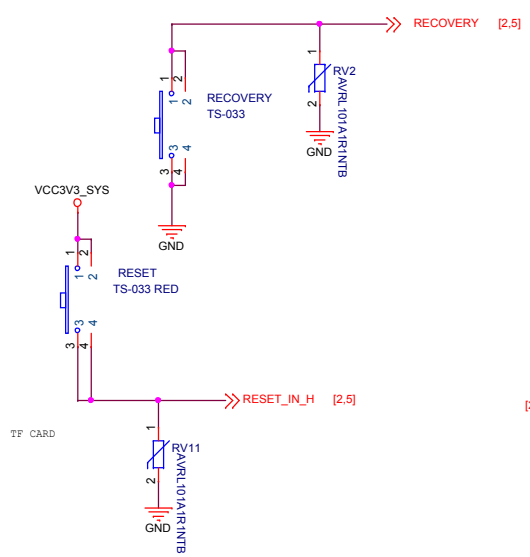
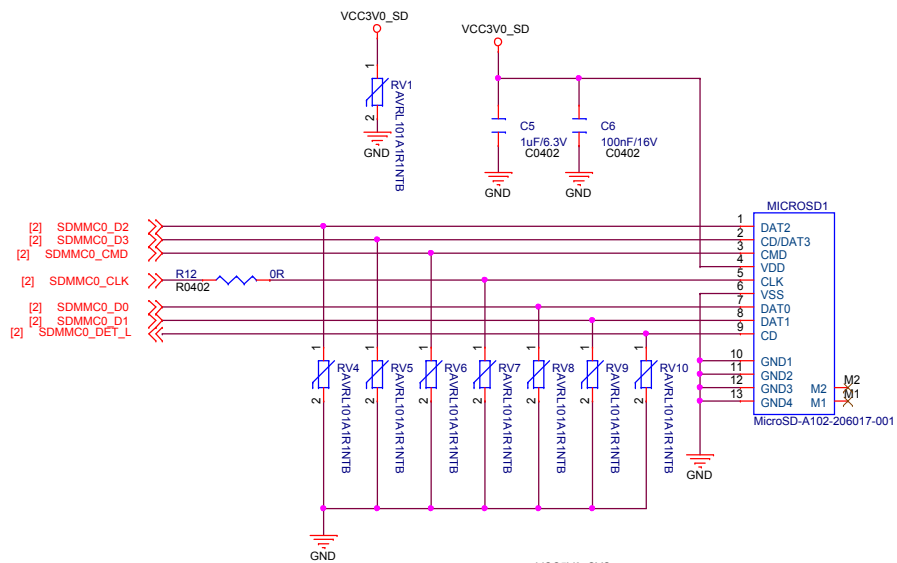




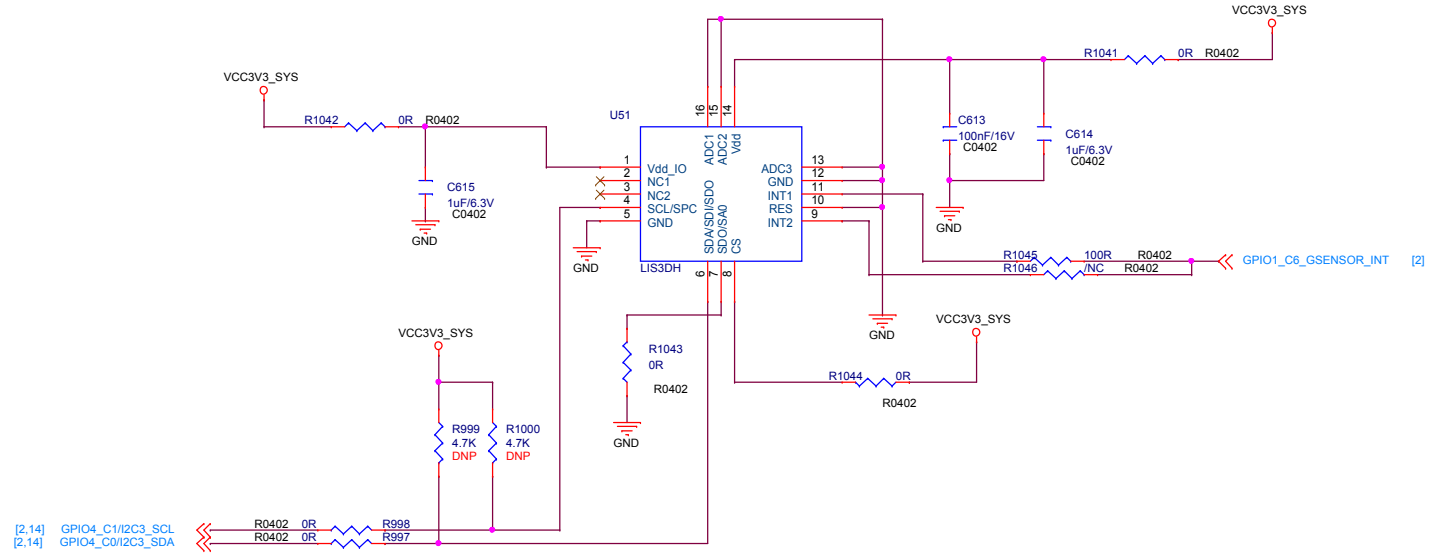
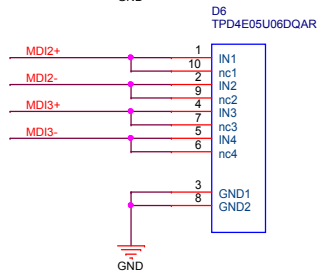
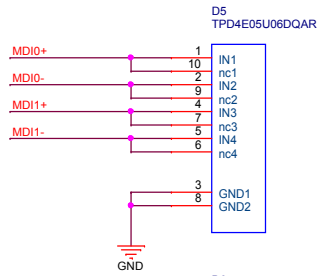
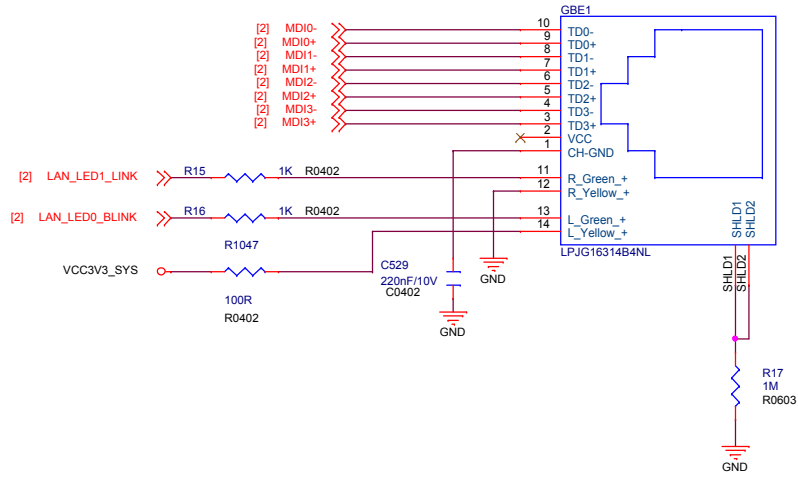


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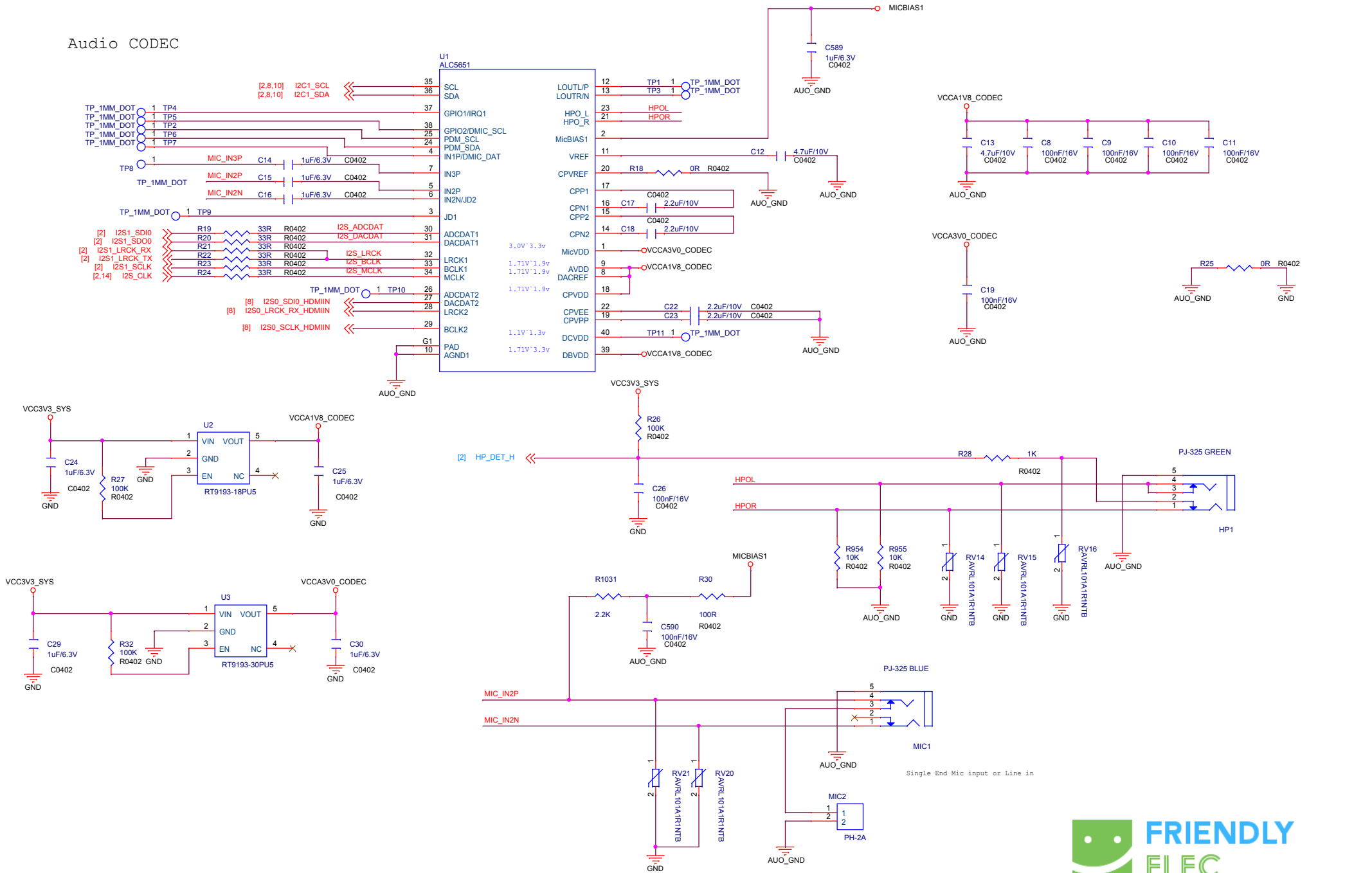
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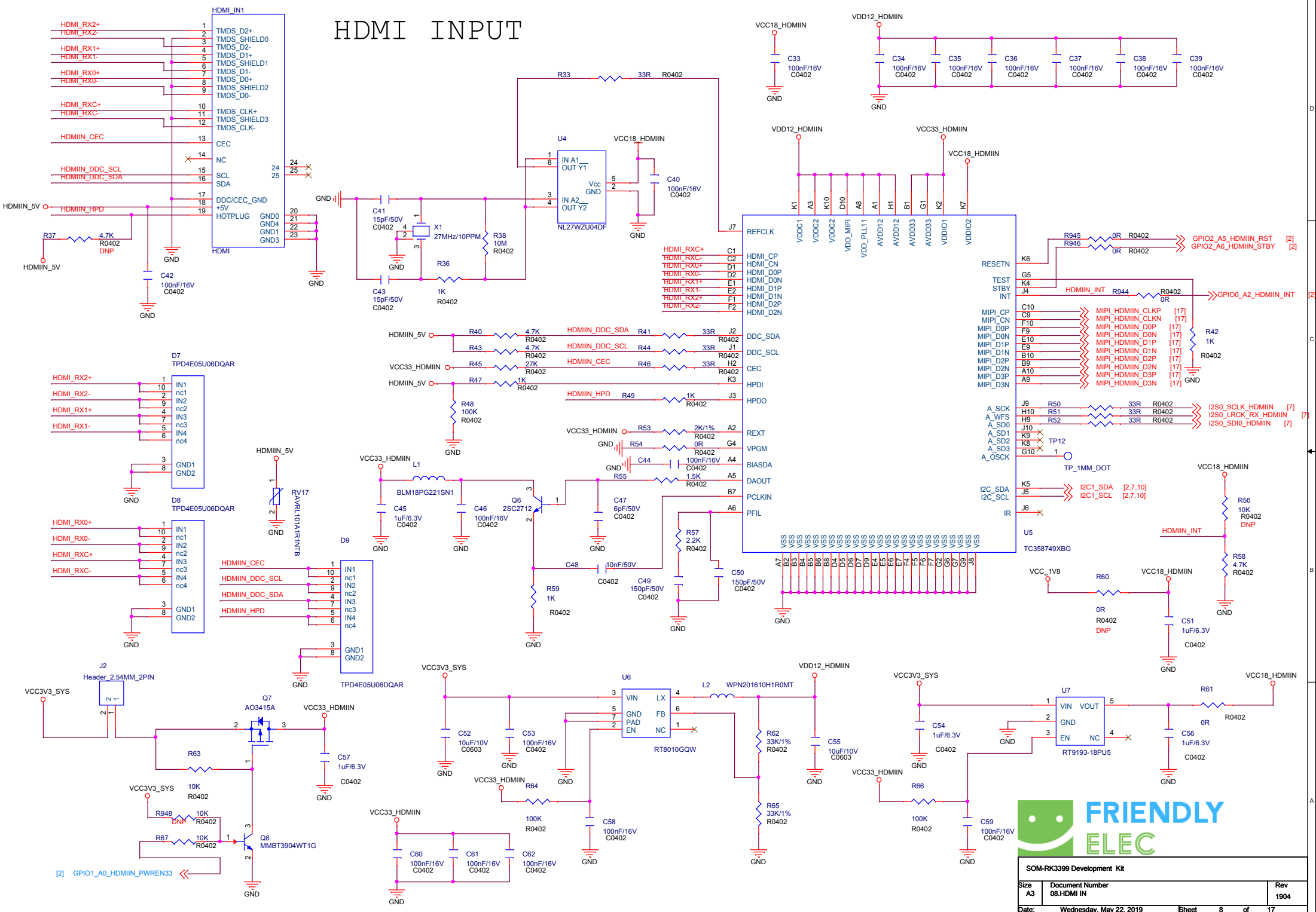
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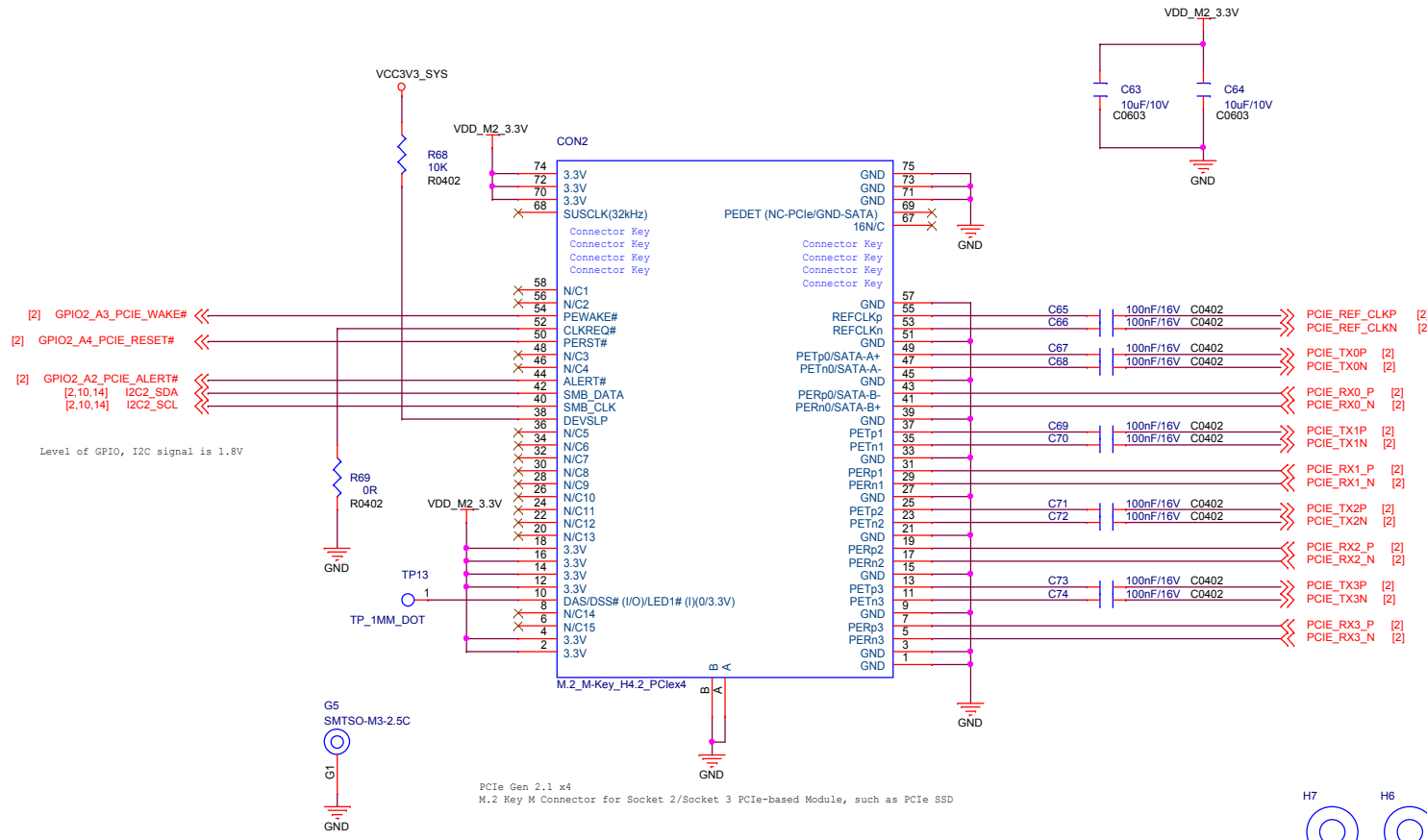
# Audio CODEC



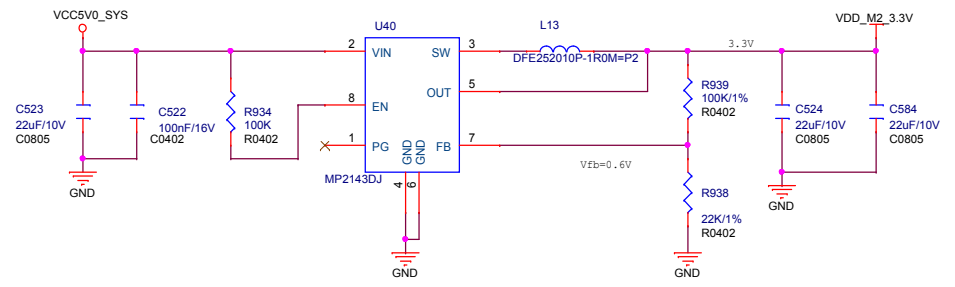
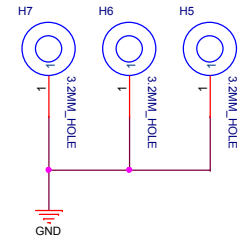
# HDMI INPUT



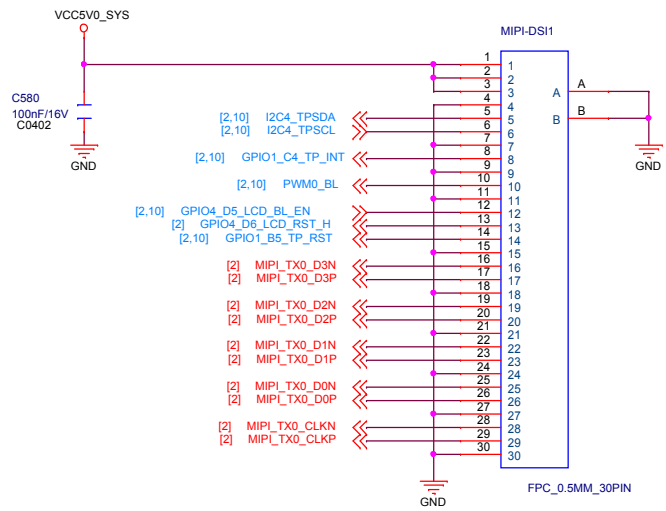




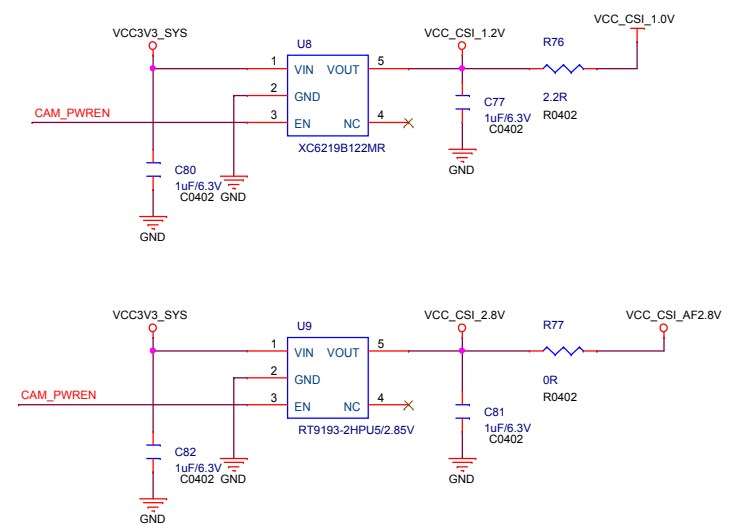
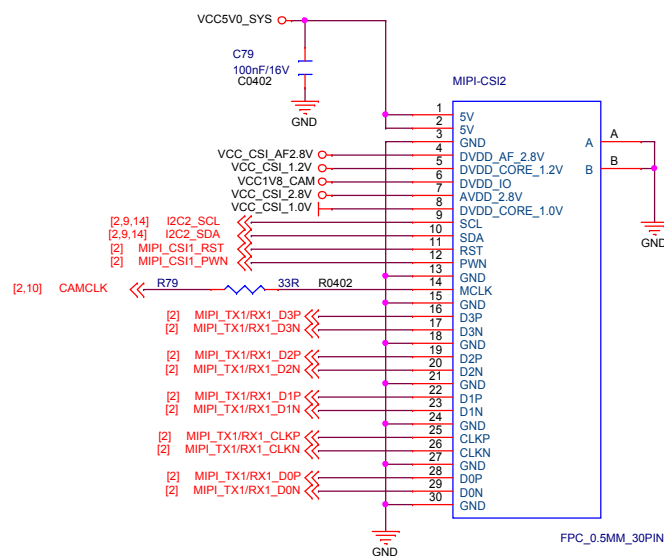
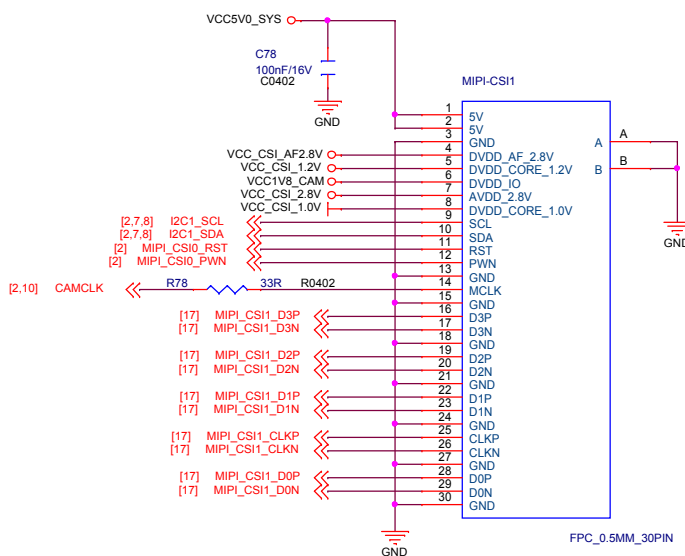
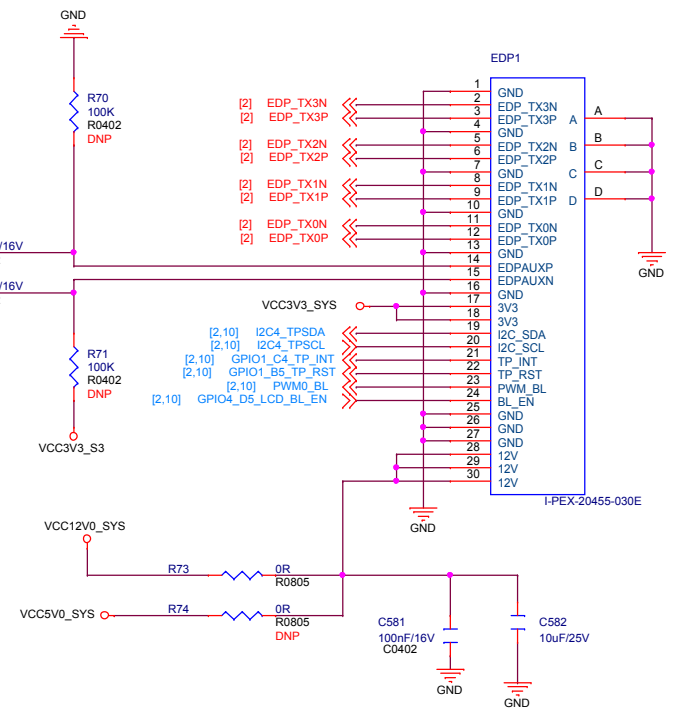
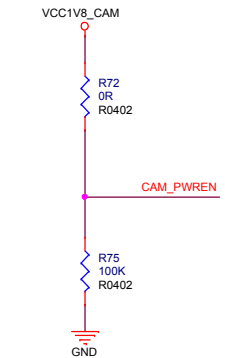
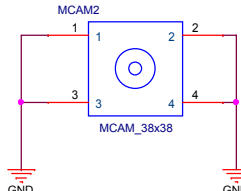
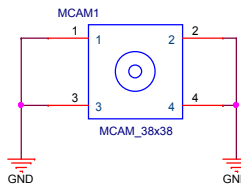
PCIe Gen 2.1 x4  
M.2 Key M Connector for Socket 2/Socket 3 PCIe-based Module, such as PCIe SSD

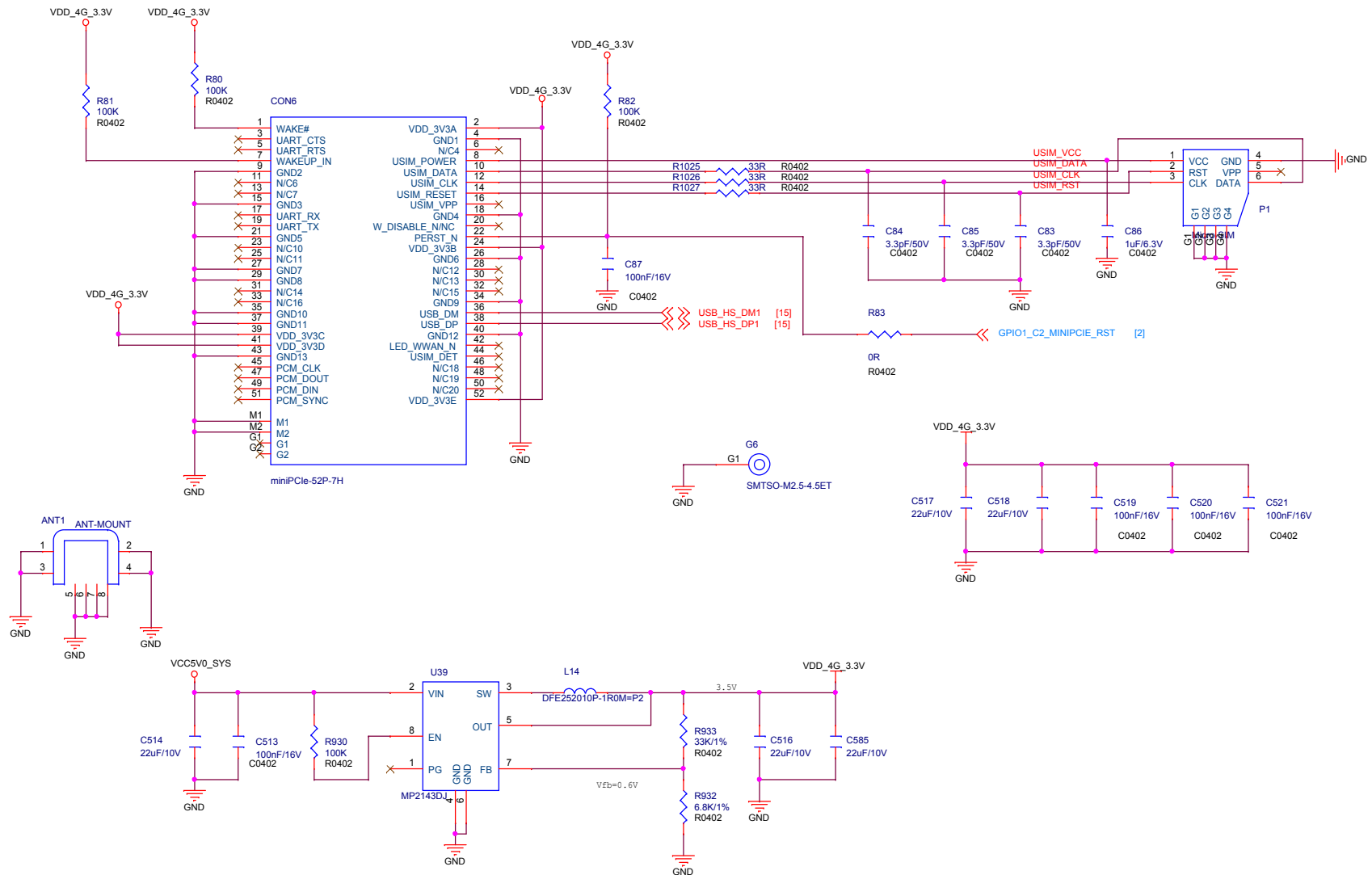


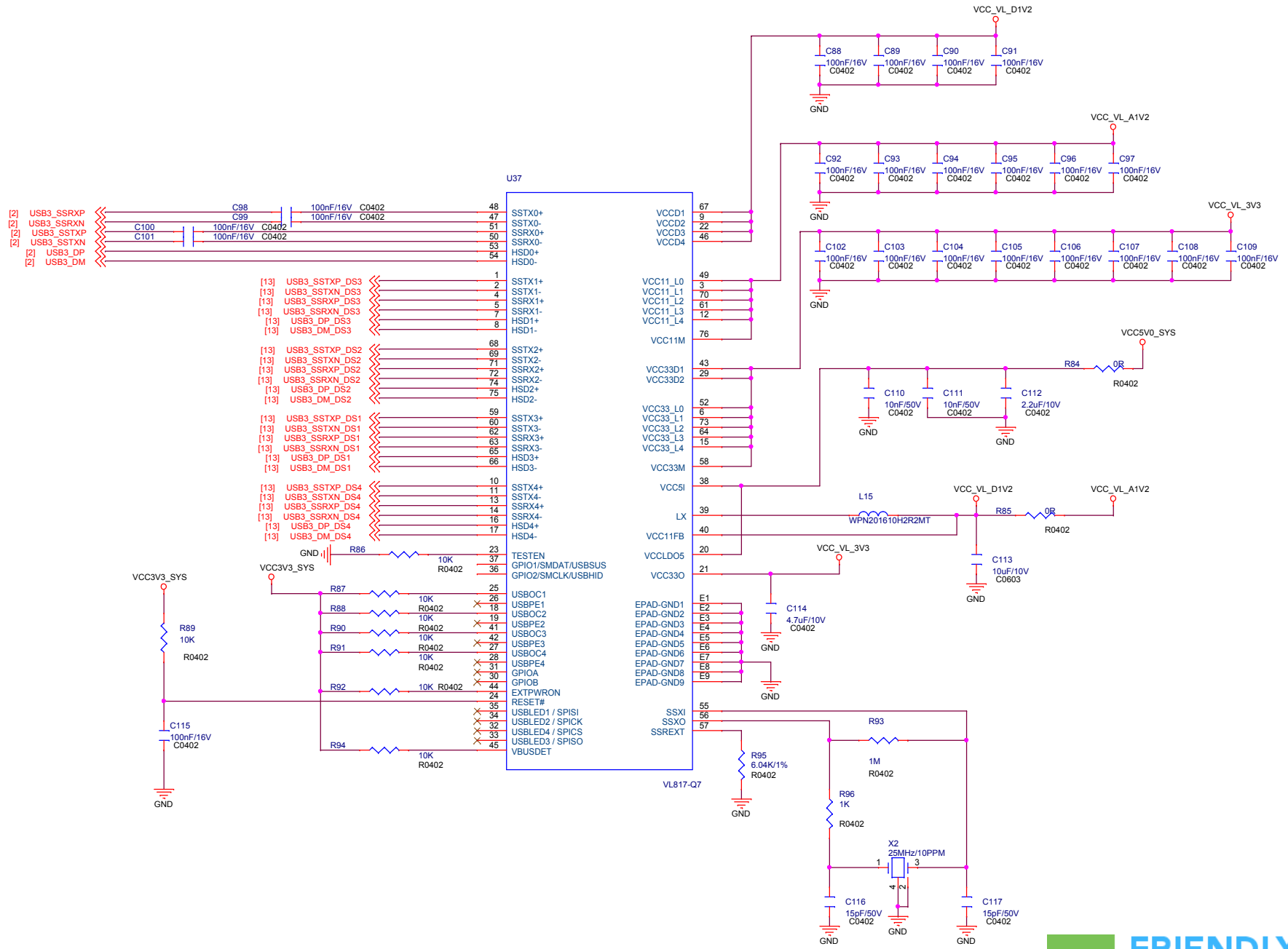
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Level of GPIOs, PWM, and I2C signal is 3.0V







# USB3.0 Host

