

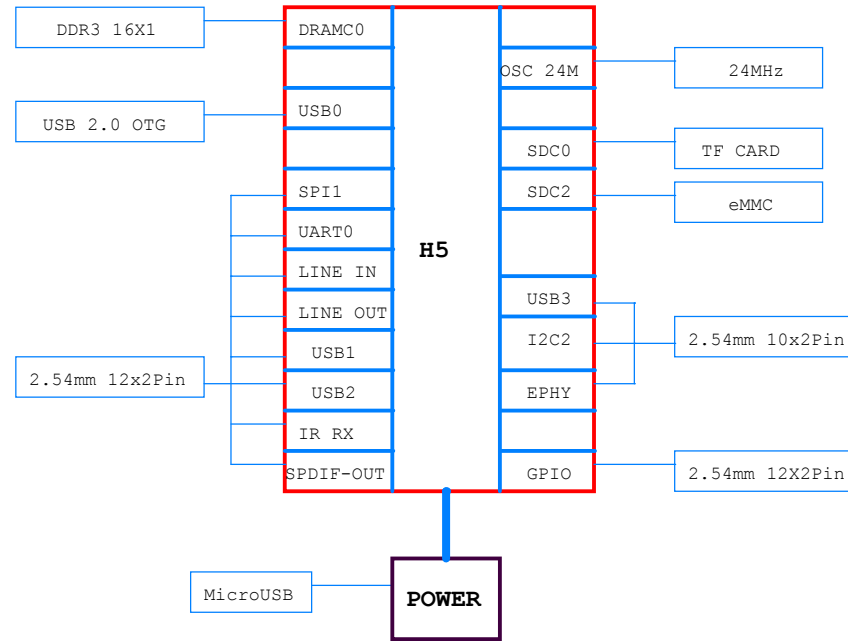
NanoPi NEO Core2

Revision
1707 , First Release

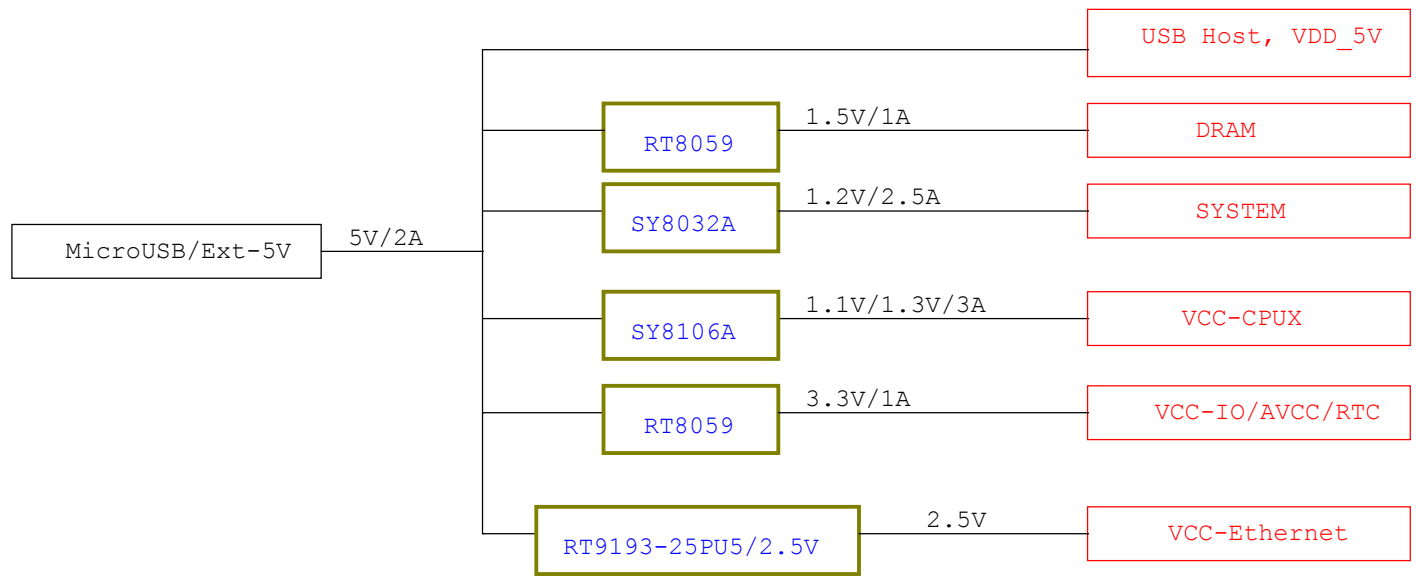


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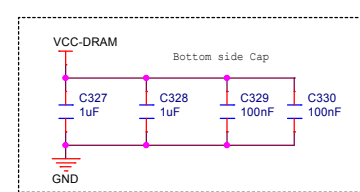
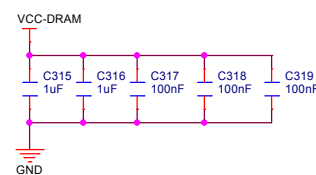
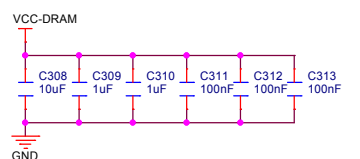
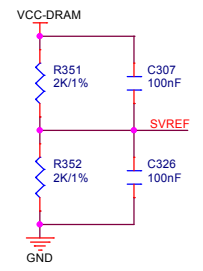
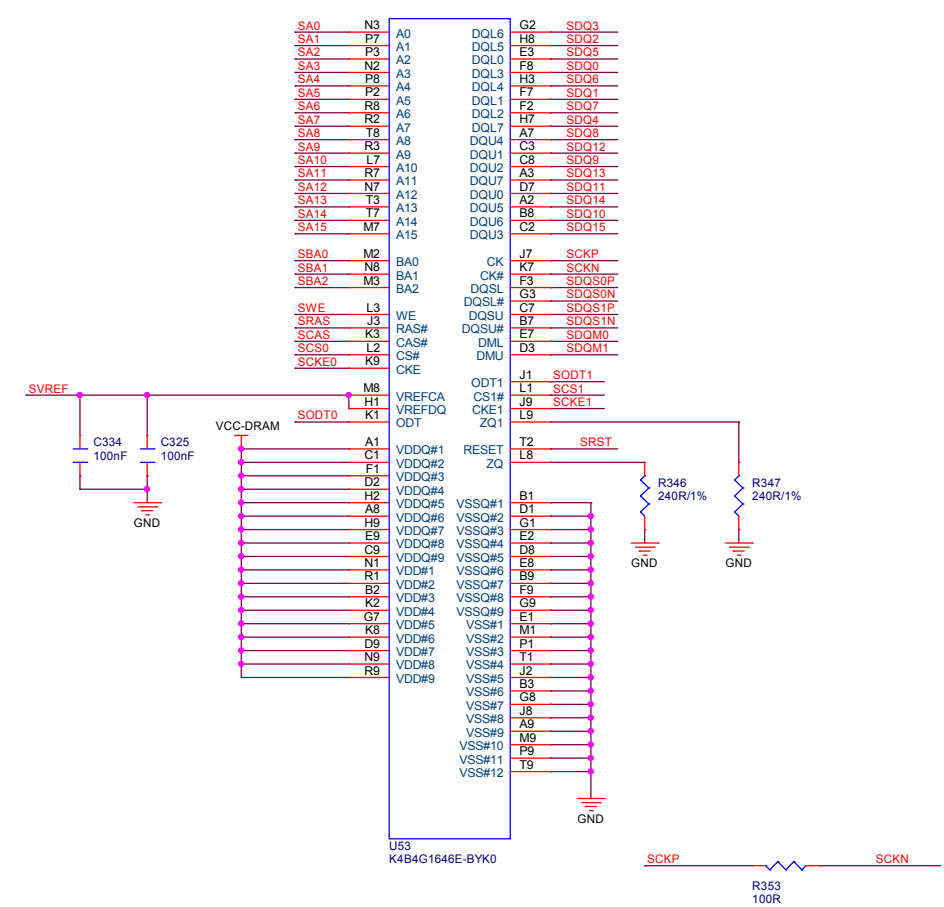
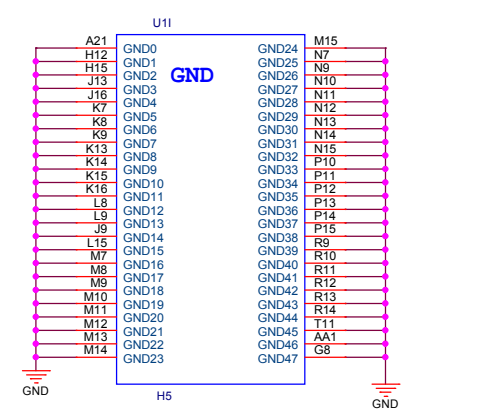
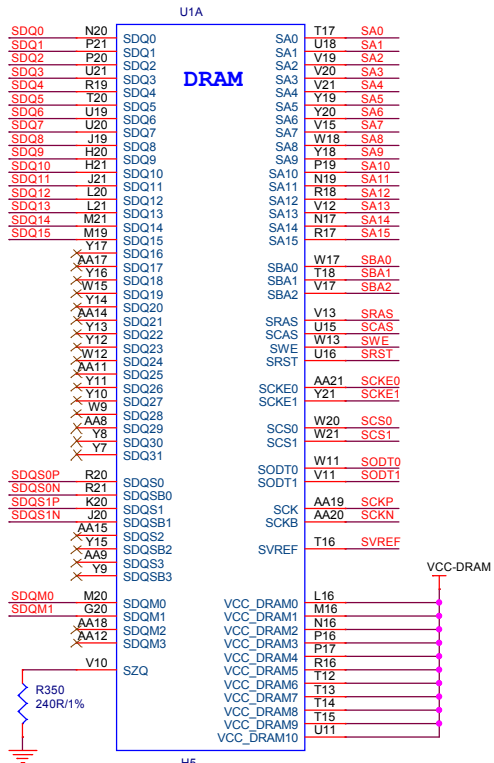
BLOCK



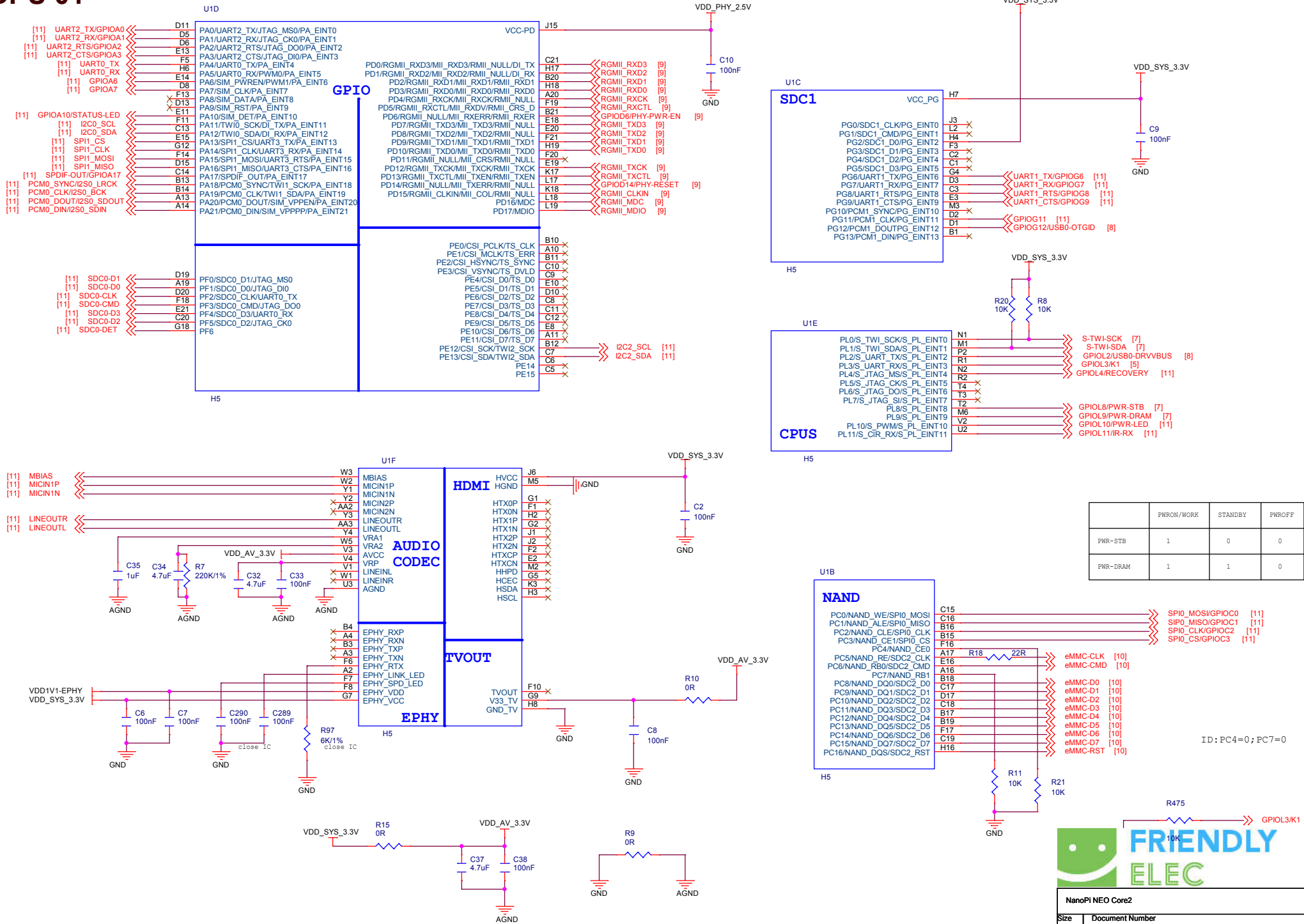
POWER TREE



DDR3 16x1



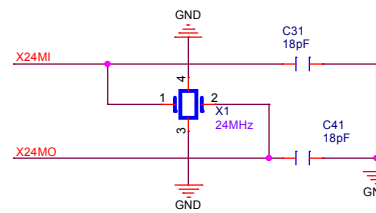
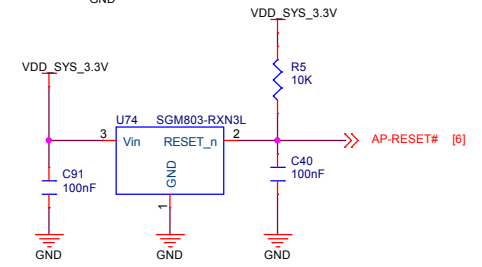
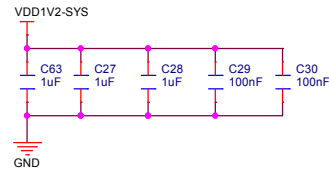
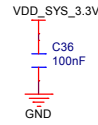
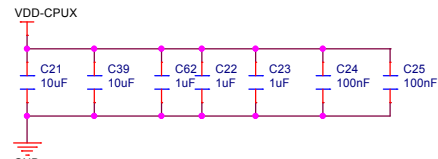
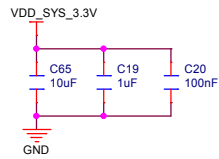
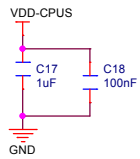
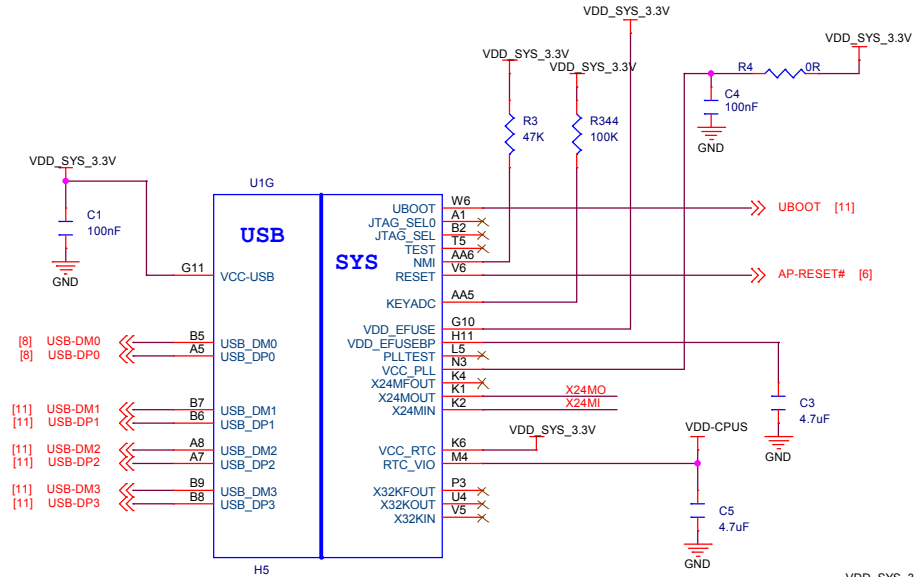
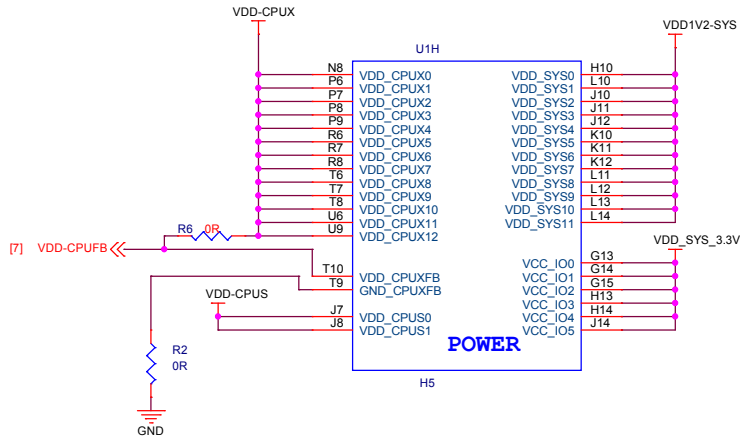
CPU 01



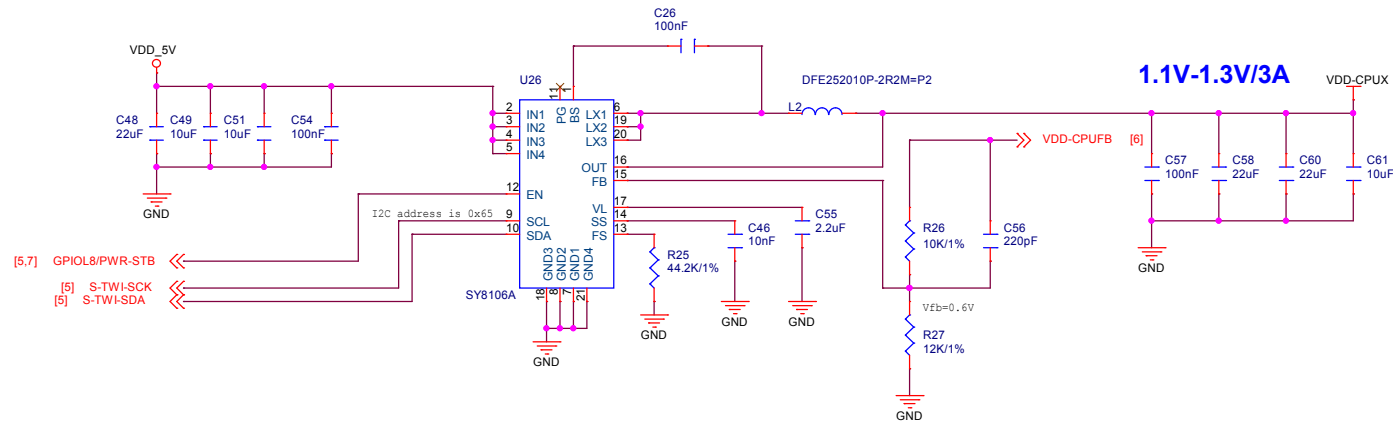
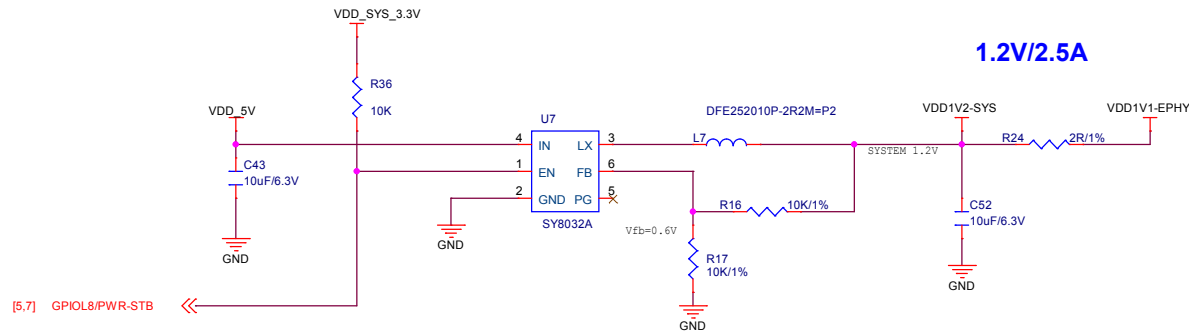
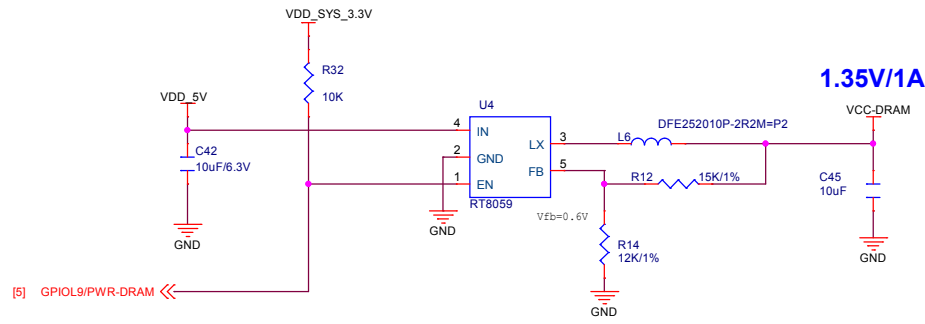
	PWRON/WORK	STANDBY	PWROFF
PWR-STB	1	0	0
PWR-DRAM	1	1	0



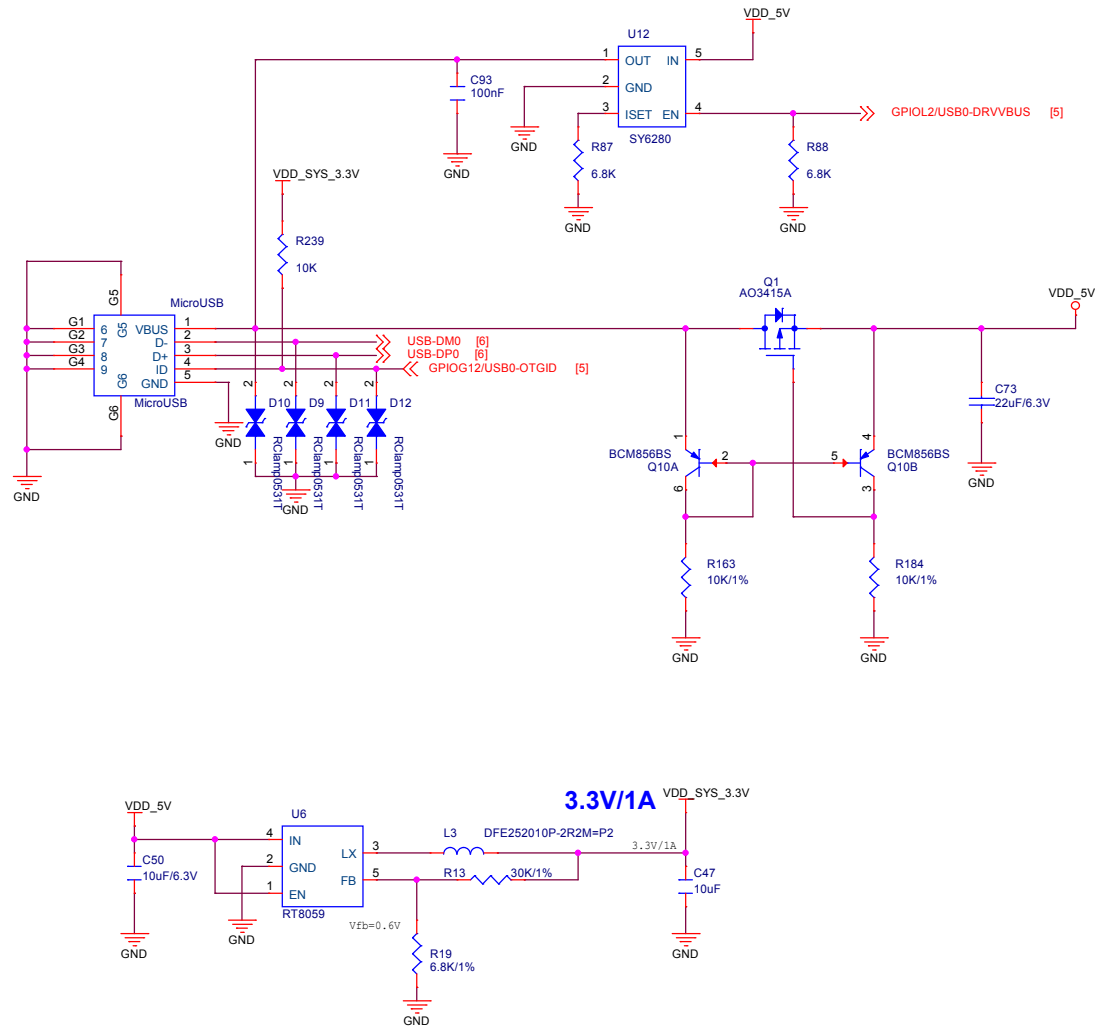
CPU 02



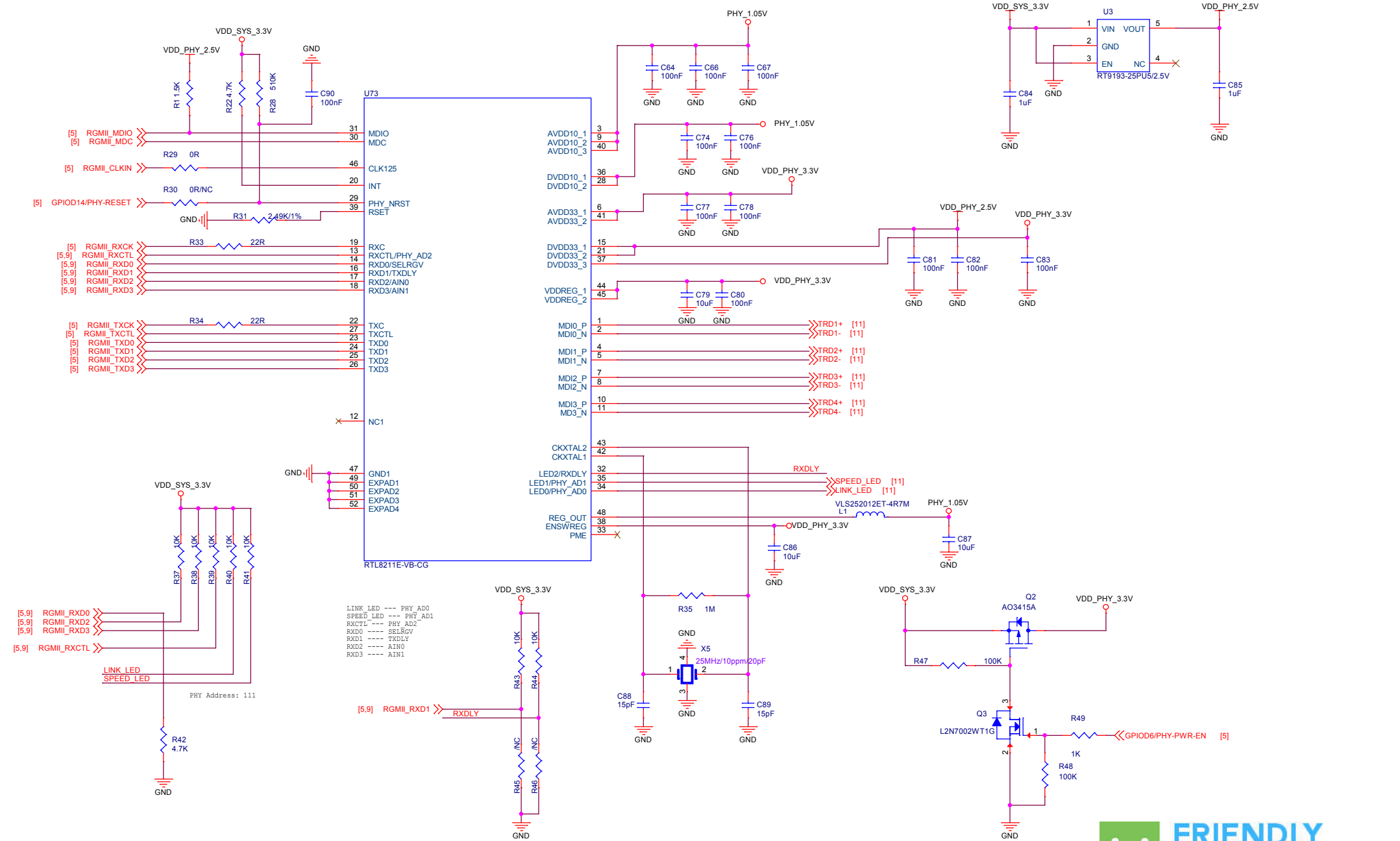
POWER 01



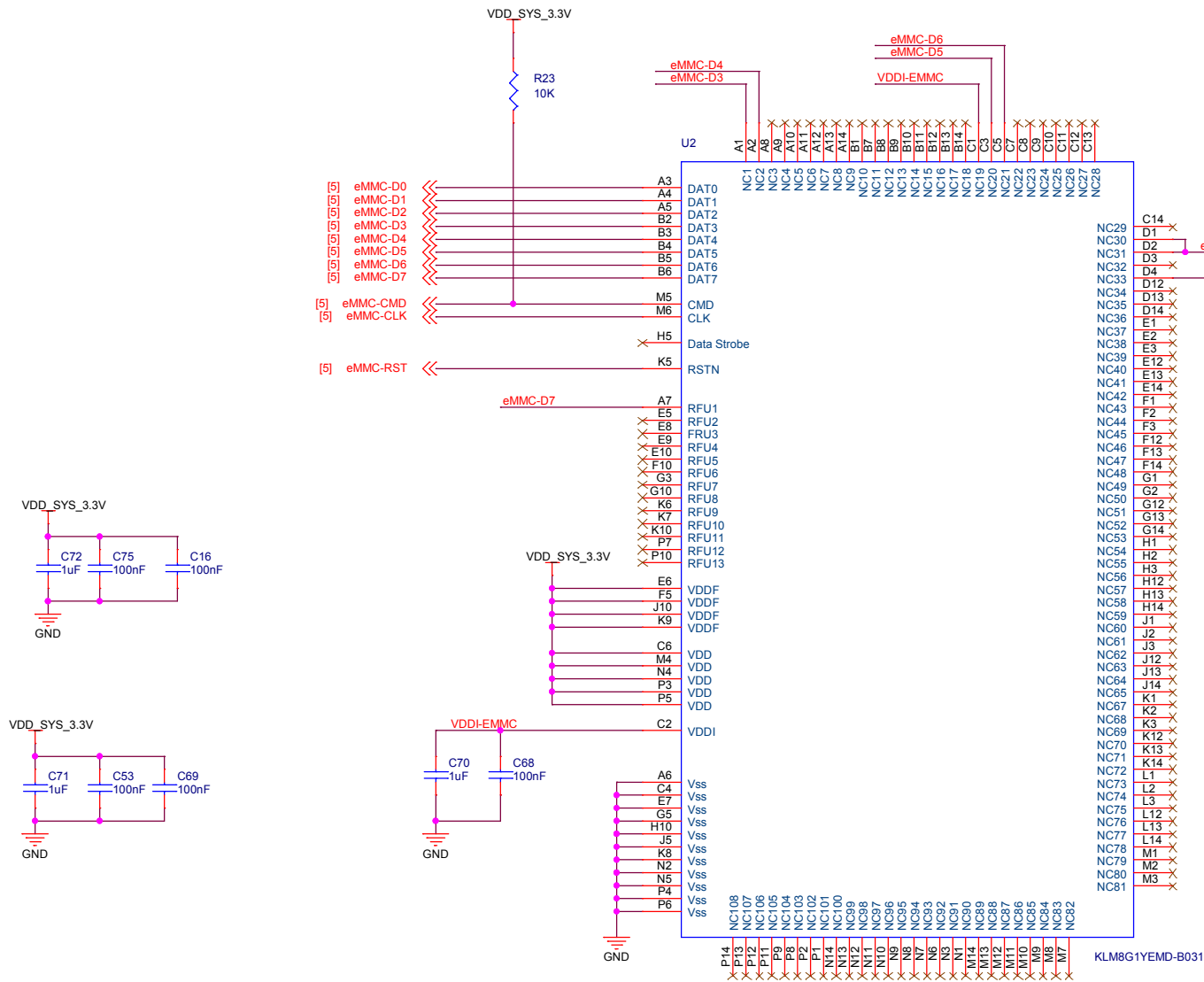
POWER 02



10/100/1000M Ethernet



eMMC



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GPIO,Card,LEDs

